

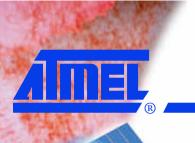
MCM and **MCGA625** status



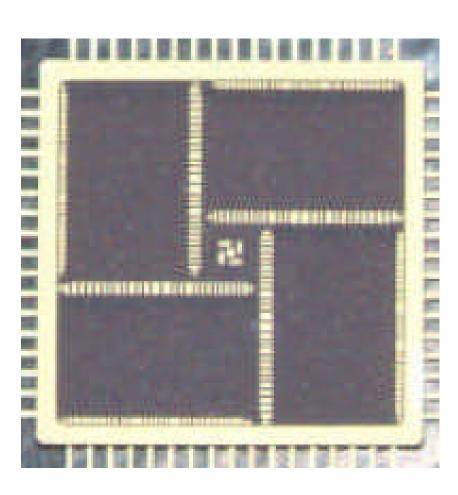




- MCM
 - Rationales
 - > MCM SRAM status
 - > MCM industrial capability
 - > MCM spin off products
- MCGA625 contract status
- Packaging roadmap



MCM





Rationales for going MCM

 DSM technologies are more and more expensive making the ROI in space more questionable

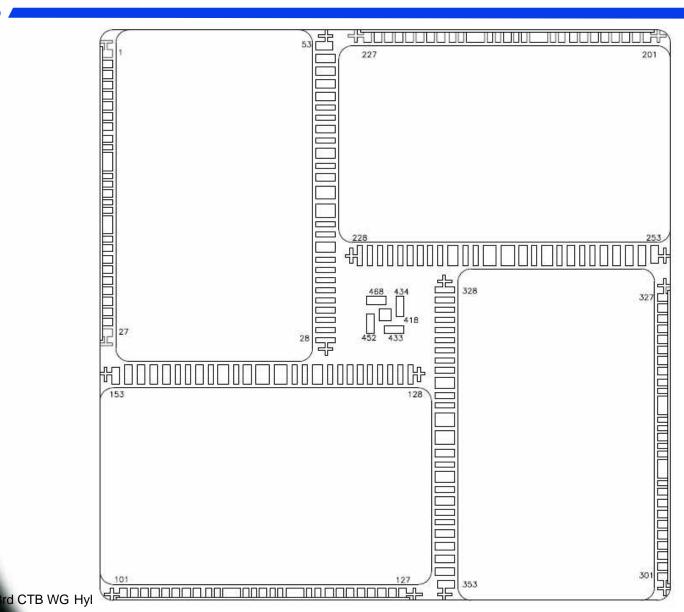
- MCM allows:
 - Either to mix various technologies and/or functionalities
 - To bridge the time gap between the availability of the next technology products
 - > To test the market for the next generation of products



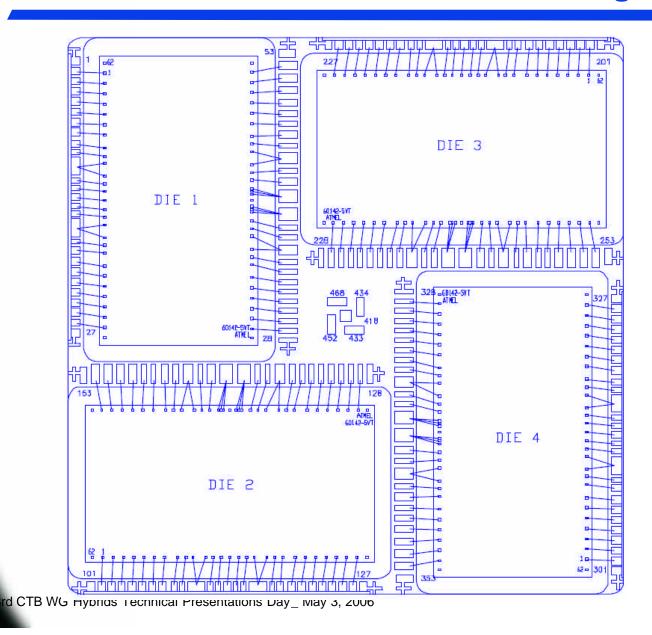
AT68166F/FT MCM SRAM description

- MQFPF_T68 package, 4 of 512Kx8 SRAM AT60142F/FT
- 1 sided cavity Kyocera package
- Same die bonding as all other products (Cyanat Ester) in one pass
- Same wire bonding (32µm Al wire) in one pass
- Same sealing process: 1 Kovar lid with Au.Sn. (80/20) pre-form
- Samples delivered
- FM with Atmel qualification July 06

MCM SRAM: cavity layout

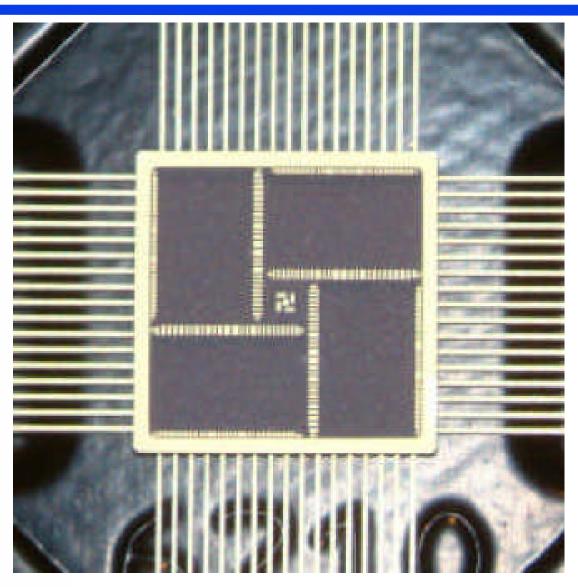


MCM SRAM: bonding diagram

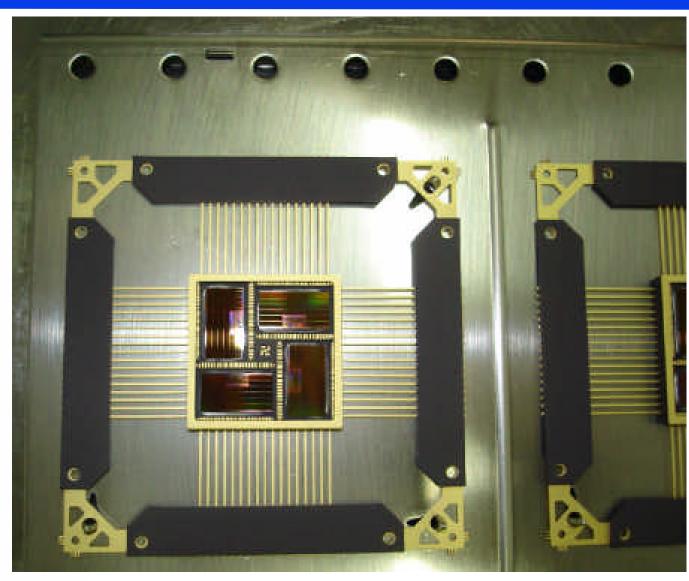




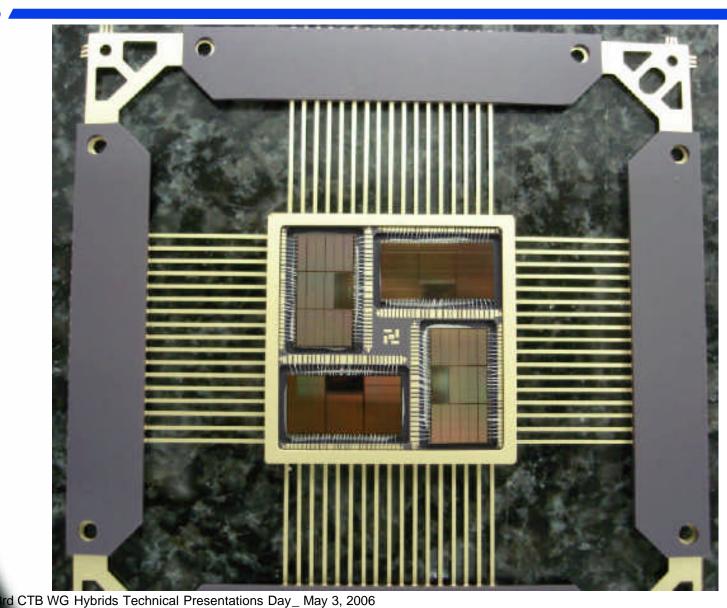
MCM SRAM: raw material







MCM with wire bonding





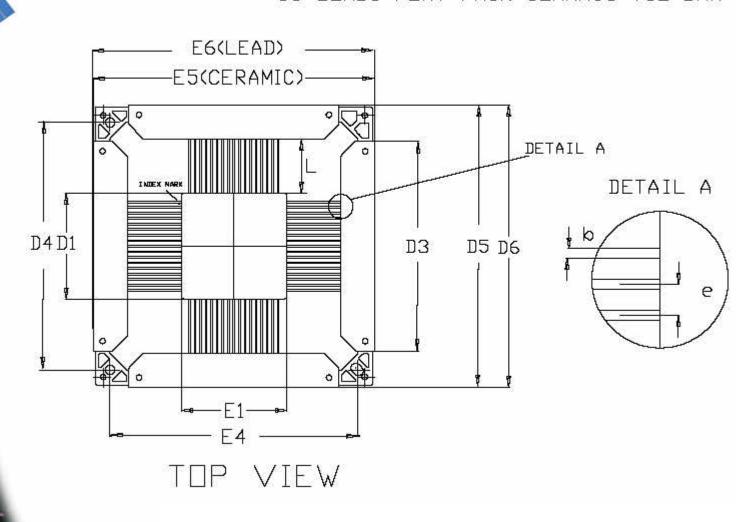
MCM sample in its packing





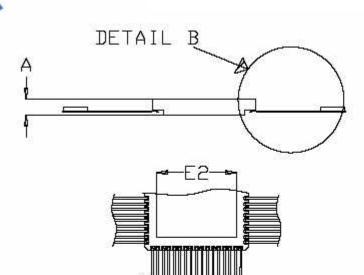
MCM SRAM: outline (1)

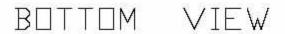
68 LEADS FLAT PACK CERAMIC TIE BAR

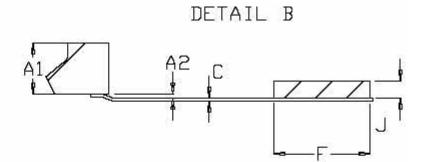




MCM SRAM: outline (2)







	m.	М	i i	nch			
	min	max	min	MOX			
E1/D1	24. 14	REF	. 950 REF				
E5	18, 30	19,00	, 720	. 748			
D3	42, 67	43, 69	1, 680	1, 720			
E4/D4	54. 36	REF	2. 140	REF			
E5/D5	63. 38	REF	2, 495	5 REF			
E6/D6	1	64, 14	1	2, 525			
J	0, 76	1, 02	, 030	. 040			
L	12, 00	REF	. 472 REF				
E	7, 62	REF	. 300	REF			
C	0, 18	0, 25	, 007	, 010			
Α	3, 72	4, 70	. 146	. 185			
A1	2, 72	3. 41	. 107	. 134			
A2	0. 20	BSC	. 008	BSC			
e	1, 27	BSC	, 050	BSC			
k)	0, 33	0, 43	, 013	. 017			



16Mb SRAM MCM: AT68166F/FT (1)

 4 off 512Kx8 SRAM die (F and/or FT) which can be used as either 1 off 512Kx32, 2 off 512Kx16, or 4 off 512Kx8

QFP68 pins, 1 sided cavity package for optimized power dissipation

3.3+/-0.3V specification, w.o. or w. 5V tolerant Inputs

25 ns address access time sampled

 Further improvements will bring the speed down to 20 ns in a first step, and later on, to 15-17 ns



16Mb SRAM MCM: AT68166F/FT (2)

Latch up immune, tested up to 300Krads

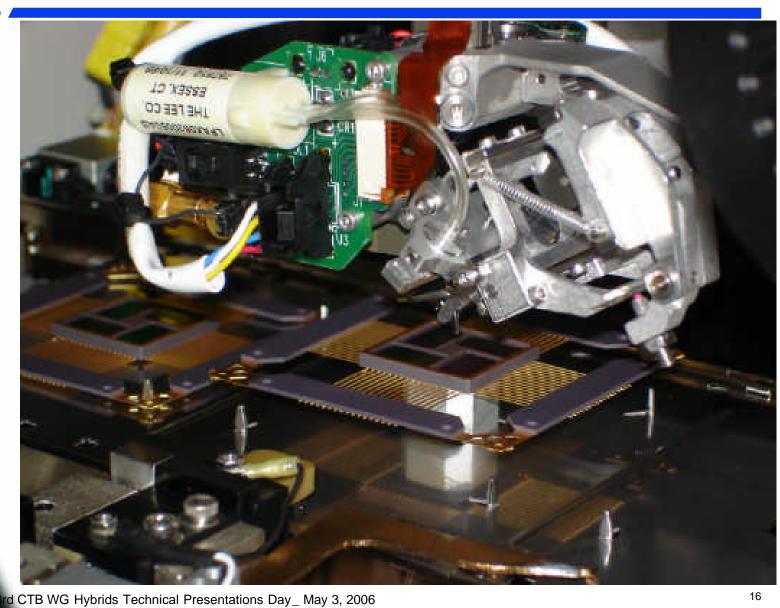
No MBU

2.5 mA stand by current

720/680 mA @ 15/17 ns & 3.3V

ESCC & QML V (5962-06229 available for 25 ns 5V Tol)

MCM wire bonder





MCM industrial capability

Die bonding:

- ESEC type MICRON equipment
- > Allows for any rotation for each die
- Up to 4 different types of die
- Cavity size: 70x70mm max

• Wire bonding:

- Kulicke & Sofa KS8060 equipment
- > Allows for any rotation for each die
- > 32µm Al wire bonding
- 63.5 x 63.5 mm for automatic bonding





EEPROM

SRAM and EEPROM

DPR & FIFO

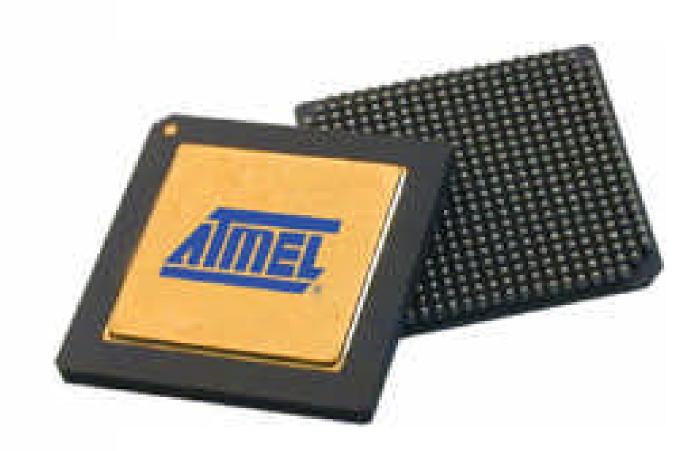
µP with caches

Customized

- Guidelines:
 - Low die count
 - ➤ Heterogeneous die MCM size must remain within the limit of the main chip (processor and caches should result in a package size of the processor's one)



MCGA625





MCGA625 contract status

- CTB-HYB-DAD 3.14.03 kicked off in December 05
- 1 mm pitch 625 MCGA
- Package design completed and samples received at Grenoble
- Daisy chain proposal sent and agreed by Astrium Velizy
- List of beta customers approved by the ESA contract technical officer:
 - > SES
 - > ASTRIUM
 - > **A2S**
- A joint evaluation plan to be coordinated by Astrium and approved by end of May



MCGA625 contract content

- Work package 1: Design and specify an MCGA625 with embedded daisy chain of connections Completed
- Work package 2: Check the feasibility of the SCI mounting on the CLGA
 Just started
- Work package 3:
 - Procure prototypes and define the screening flow (test socket insertion, burn-in temperature...)
 - Deliver daisy chain to beta customers
- Work package 4: Develop a cavity compatible with the biggest ATC18RHA ASIC size, the ATC18RHA95_504
- Work package 5: Final report



MCGA625 sampling plan

Albatol Albina Opado.	
Alcatel Alenia Space, Toulouse, France	8#

Alcatel Alenia Space, Roma, Italy
8#

Alcatel Alenia Space, Charlerois, Belgium 8#

Astrium, in Velizy, France16#

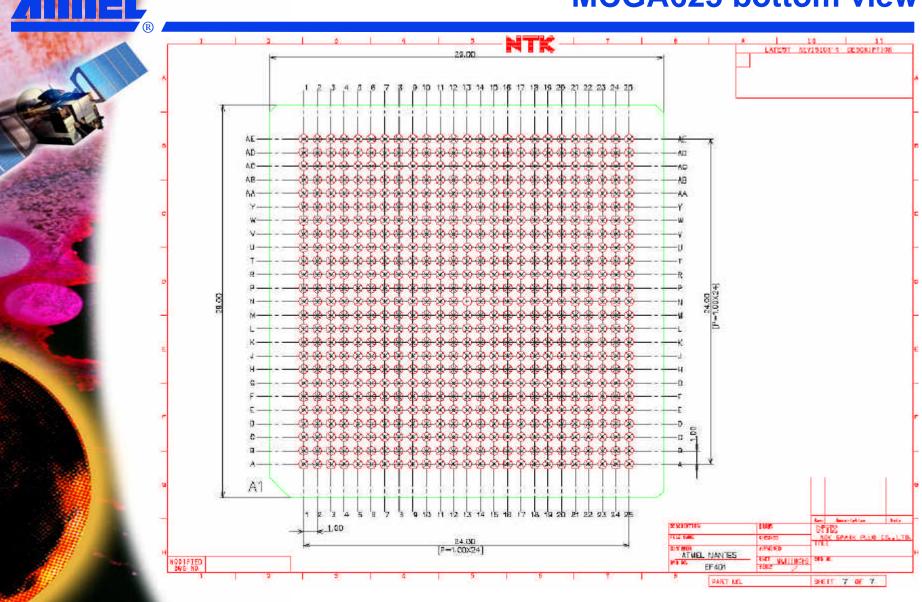
Saab Ericsson Space16#

• TOTAL 56#

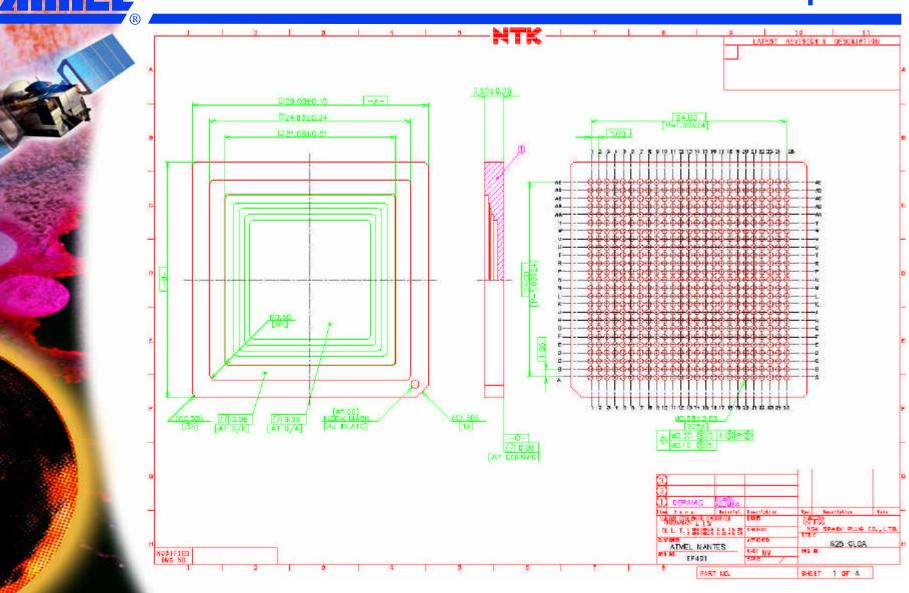
Schedule

	01/06	02/06	03/06	04/06	05/06	06/06	07/06	08/06	09/06	10/06	11/06	12/06	01/07
Work package 1													
Definition of LGA & SCI	=												
Fabrication of SCI	===	===	===	===									
Fabrication of LGA	===	===	===	===									
Work package 2													
Reflow profile definition	===												
Centering tool design	===												
SCI attachment					==	II							
RX inspection						II							
Report validation						==	II						
Electrical tests							II						
Work package 3													
Daisy chain assembly								==					
Daisy chain test									==				
Daisy chain sent to beta										II			
Work package 4													
New cavity design				===	===	===	===	===	===	===	===		
Work package 5													
Final report												===	===

MCGA625 bottom view

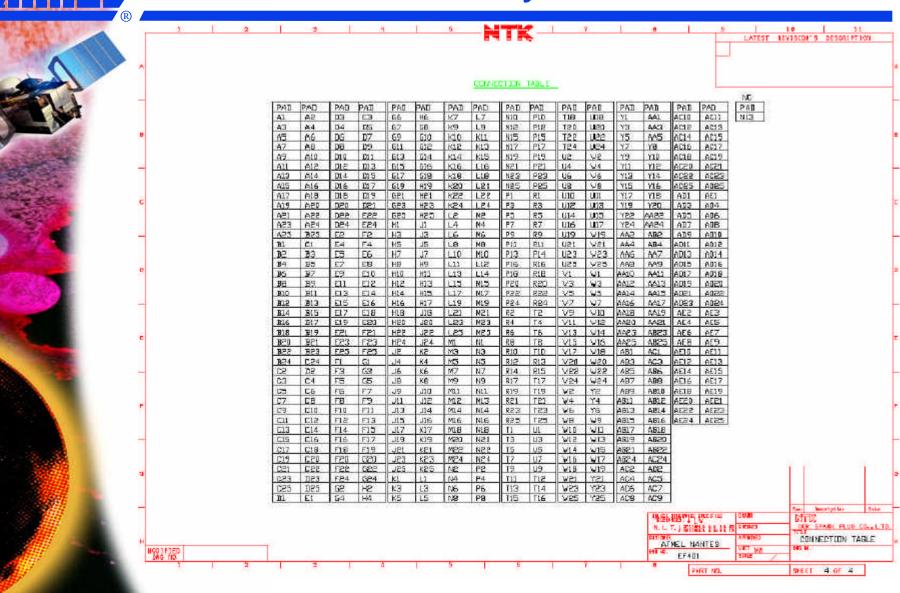


MCGA625 top view

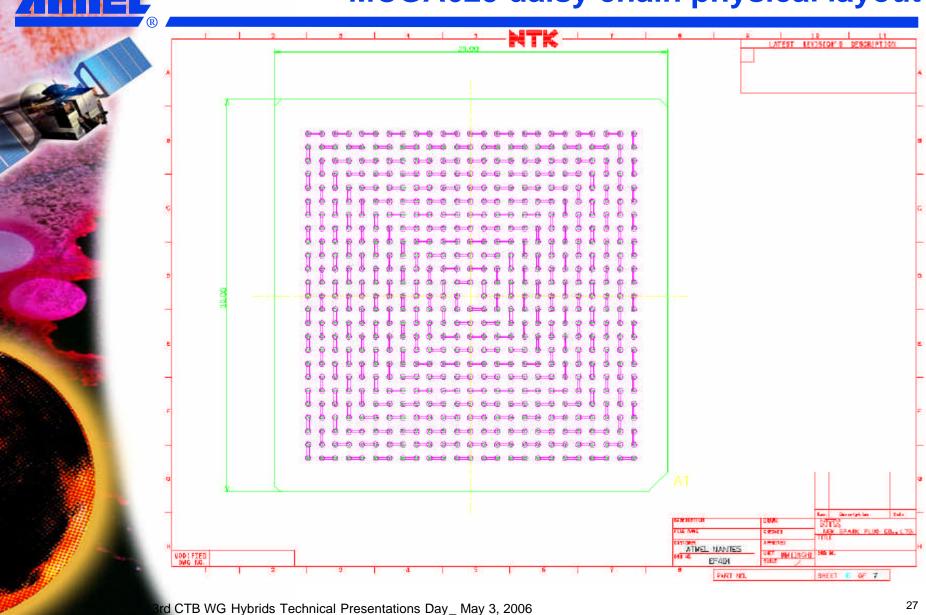


rd CTB WG Hybrids Technical Presentations Day_ May 3, 2006

Daisy chain connection table



MCGA625 daisy chain physical layout





Ceramic packages offering and roadmap

