

SECTION 9. JUNCTION FIELD-EFFECT TRANSISTORS

9.1. INTRODUCTION

The silicon junction field-effect transistor (JFET) has many of the characteristics required to withstand heavy radiation damage. Amplification is produced by the transport of majority carriers through a channel which is quite heavily doped. Unlike the MOSFET, current flow is remote from the surface of the silicon. Thus, both the major effects which degrade bipolar and MOS transistors in a radiation environment (ionisation and bulk damage) are absent. Commercial JFET devices have indeed proved in practice to be tolerant of the effects of heavy ionisation and bulk damage, and some work has also been done in the USA to produce special JFET devices which are even more tolerant of neutrons. Despite these advantages, the use of the JFET in "hardened" equipment has not been extensive. For space environments, where ionisation is dominant, the advantages are not as great as in nuclear environments. However, in the field of instrumentation amplifiers, the properties of the silicon JFET as a low-noise, high-impedance input device have already been appreciated in commercial use and are attractive for some uses in space. The gallium arsenide JFET is now increasingly used in microwave circuits.

As expected, gamma and particle irradiations to a total dose of 10^6 rads cause only minor degradation in JFET (Martin, 1985; Rudie, 1972). Normally, transconductance, pinch-off voltage, "on" resistance and other channel parameters are practically unchanged. Due to oxide charge build-up, surface leakage may manifest itself as an increase in I_{GSS} , but this appeared to remain in the nanoampere range under the highest doses used. Reactor neutron irradiation causes minor degradation at 10^{13} n.cm⁻², but severe degradation at 10^{15} n.cm⁻².

Notthoff (1971) finds that matched pairs of discrete commercial JFET devices, which are suitable as input devices for a differential amplifier, operate well after exposure to a reactor neutron fluence of about 10^{14} n.cm⁻². Compared with bipolar equivalents, integrated operational amplifiers with JFET structures as input devices may therefore be preferred for very high radiation environments. Other radiation-hardened circuits that include JFET's are multiplexers and ladder networks for data-acquisition circuits. An application which suggests itself for future space use is as the amplifying element for a highly exposed photodetector, say in the parts of a data link which must survive for a long time in a robot exposed to reactor or RTG radiation. A "Photo JFET" would consist essentially of a photodiode with built-in amplification by the JFET mechanism. No test data are available for this type of device. However, if radiation problems associated with amplification or light detection arise in future

projects, the possibilities of JFET devices should be investigated in some detail.

A more specialised device that has been finding commercial use lately is the microwave JFET in silicon or gallium arsenide. The mobility of III-V semiconductors leads to good high-frequency operation. GaAs FETs are used routinely as low-noise input devices for microwave amplifiers, while very fast logic devices are under development. Several investigations have shown that neutron irradiation of GaAs FETs has the effects predicted. Studies of bulk properties on III-V compounds, for example, have shown the reduction of conductivity. As for silicon devices, the resulting device degradation only becomes apparent in the range 10^{14} to 10^{15} n.cm⁻² (1 MeV).

Several authors emphasise that the JFET structure is "hardenable" and the choice of electronic devices in very high particle fluxes may often lie between vacuum tubes and specially "hardened" JFETs. This choice applies to amplification, switching and light detection.

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