



New materials for next generation of microwave packaging

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EMPPS'2014

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Trends on new materials for microwave packaging

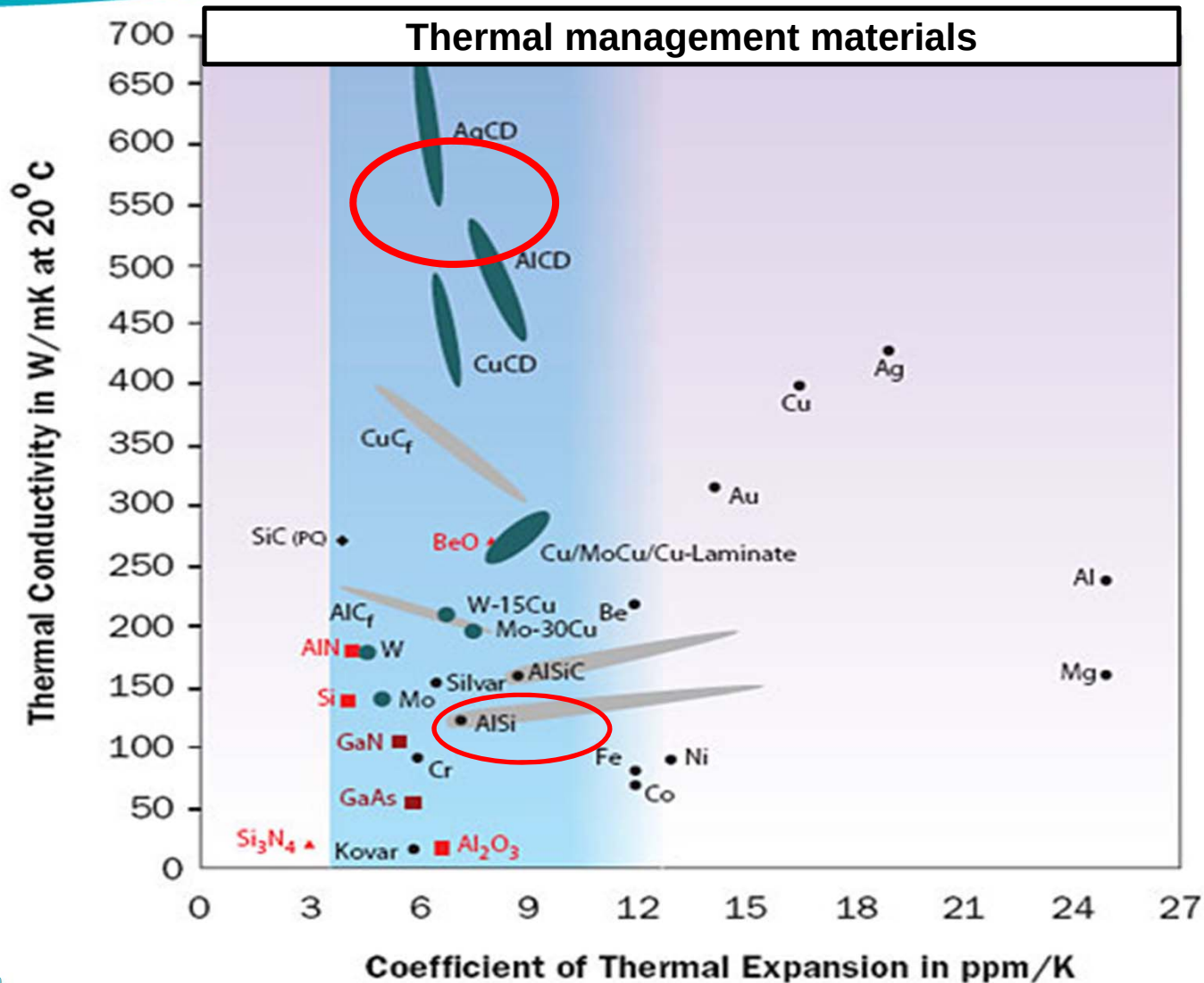
New interface material for thermal management

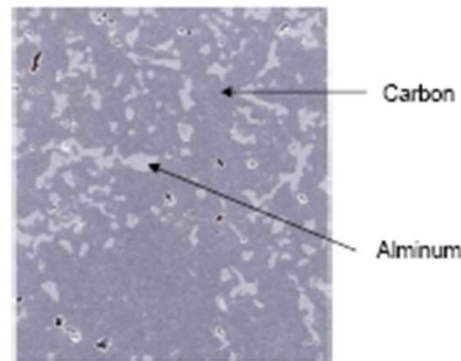
Multifunctional ceramic substrate for embedded passive components

New laminate substrate for complex PCBs

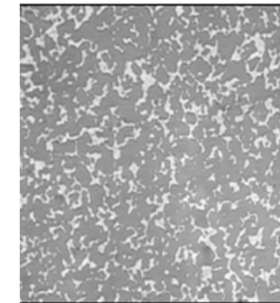
Advanced interconnects using microelectronic techniques

Conclusions





Cross Section of AIC (x 200)

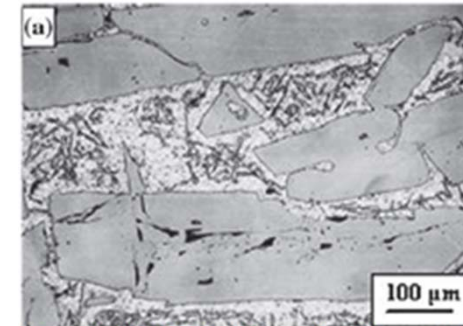


Microsection AISi (CE7)

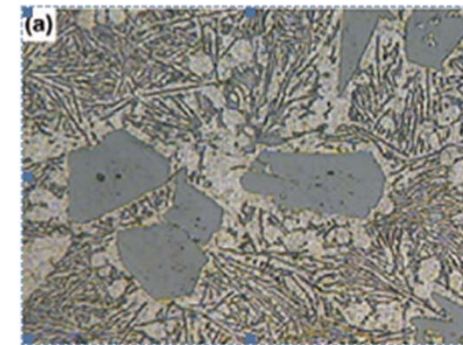
	Kovar / Dilver	Aluminium	AISi (CE 9)	AIC
densité	8.4	2.8	2.5	2.1
conductivité thermique (W/m.°C)	17.5	238	140-180	150
C E T moyen (ppm/°C)	5.9	23	9	6.5

These material are fragile which limits their use to small hermetic packages and non hermetic carrier

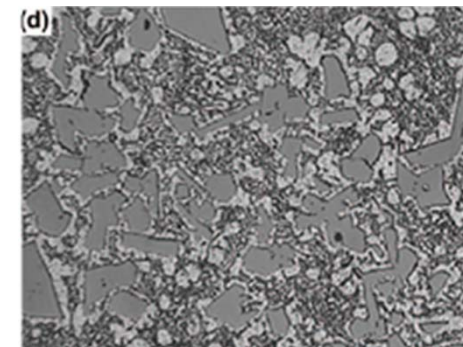
SiAl alloy with large Si crystal
Flexural strength <60 MPa



SiAl alloy with medium Si Crystal
Flexural strength <100 MPa

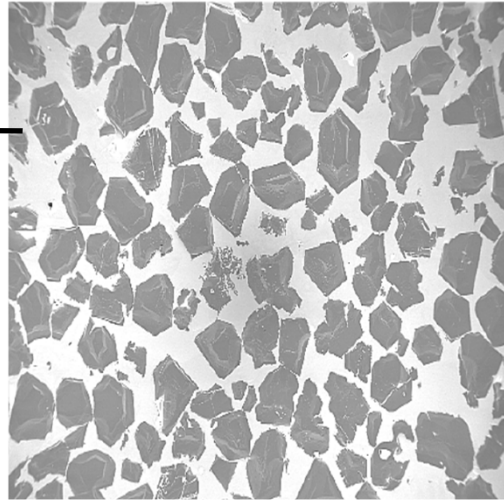
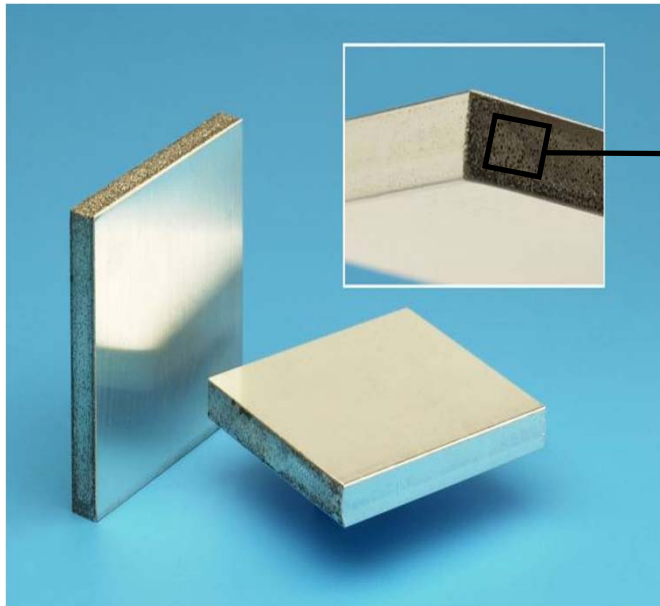


SiAl alloy with small Si Crystal
Flexural strength <150 MPa

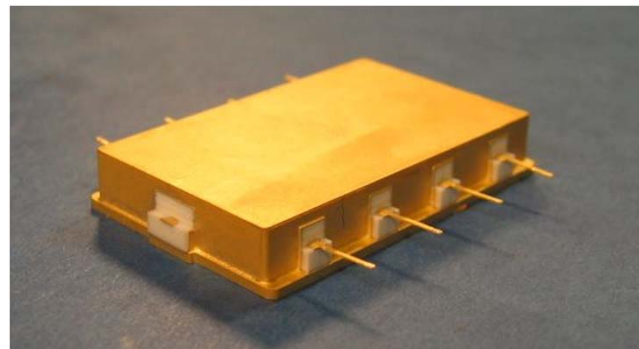


Rounded shape of Si Crystal has also a

Composite material : high thermal performance MMC

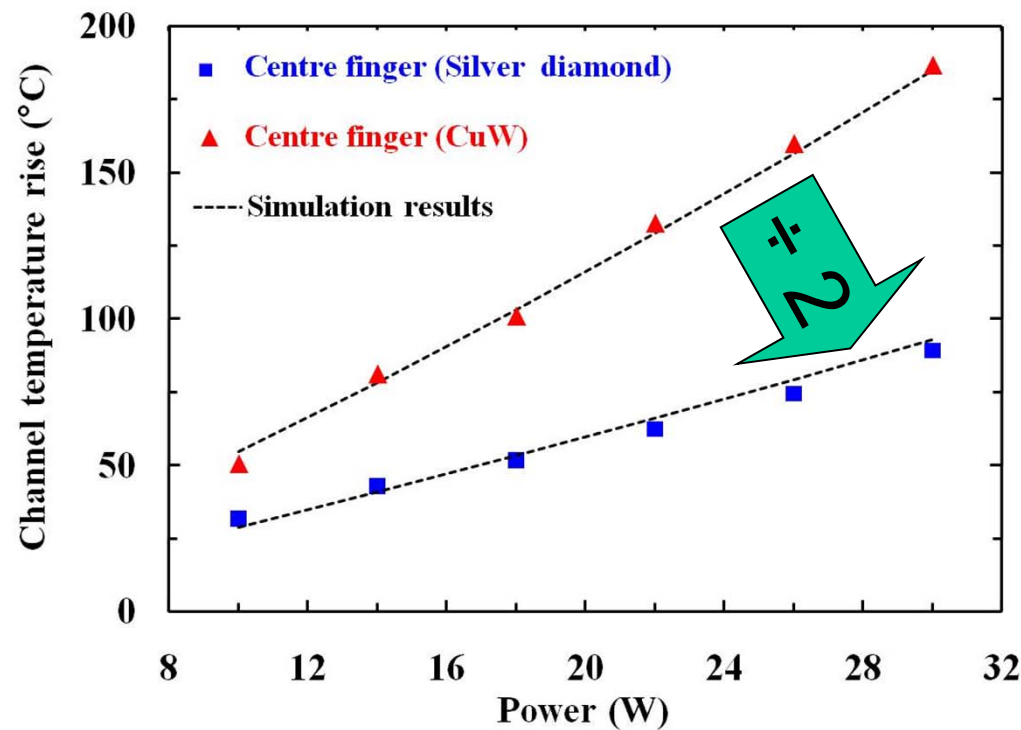


**Diamond silver
composite ($T_c > 800$
 $W/m^{\circ}C$)**



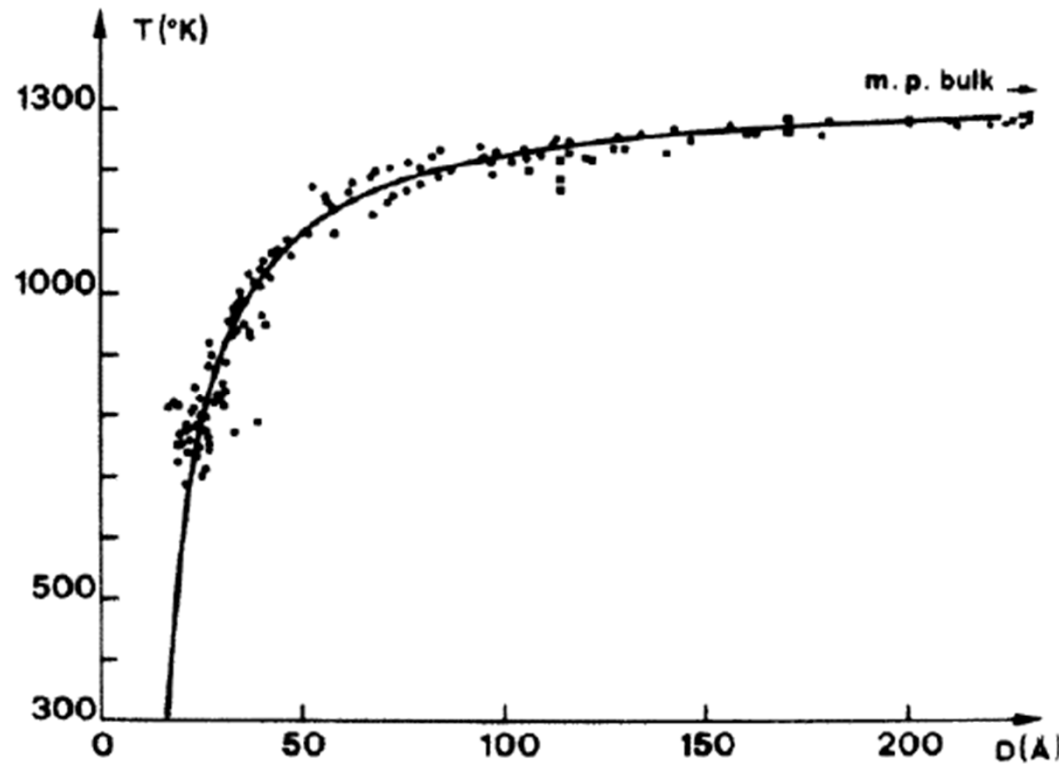
**Micropackage for L
band power amplifier**

Junction temperature measurement done on GaN power bar mounted onto AgCD and WCu micropackage using μ Raman method



With same dissipated power, junction temperature is divided by 2 with AgCd

Size effect on melting temperature of gold particule



P. Buffat and J.-P. Borel, "Size effect on the melting temperature of gold particles," Physical Review A, vol. 13, no. 6, p. 2287, Jun. 1976

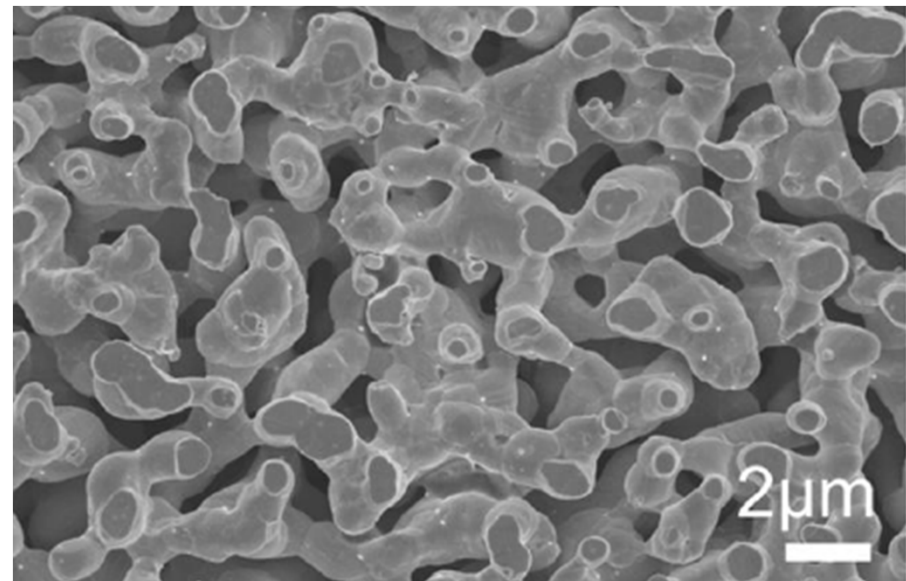
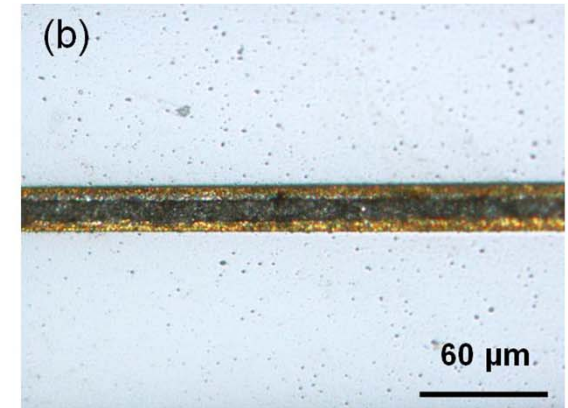
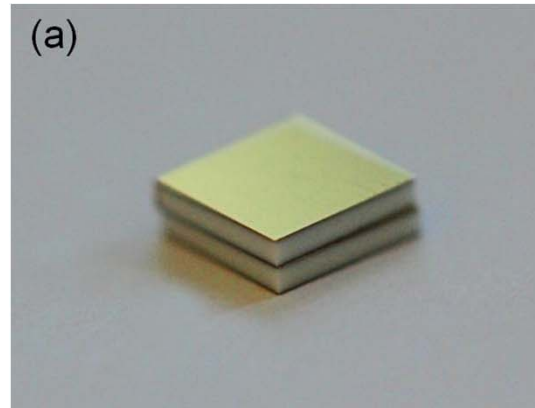
Up to 100 W/m°C pure silver interface

Sintering done at low pressure at temperature lower than 300 °C

No flux necessary > formation of AuAg alloy

Interesting candidate to replace AuSn eutectic alloy

Patent pending on specific product non affected by REACH



Size reduction at low frequencies, low loss and temperature stability at high frequencies.

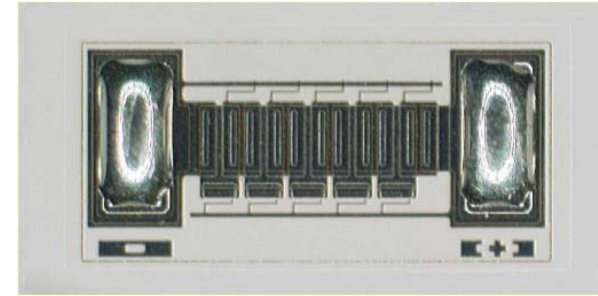
Material	ϵ_r	$\tan \delta$ 10^{-4}	CTE ppm/°C	TC W.m ⁻¹
Quartz- SiO ₂	3.75	1.5	0.5	1.5
Cordierite	4.9	10	3.5	3
Steatite	6-6.5	6	7-9	2-4
Forsierite	6-8	2-3	8.5-10	3
BeO	6.6	2	7.5	260
AlN	8.5	3-10	4.5-5	107
Al ₂ O ₃ 99.6%	9.2	2	7.2	33
Al ₂ O ₃ 96%	10.2	2	7.5	21
Mg, Zn	20	1.5	8	7
Zr, Sn titanate	35	1.5	7.4	2
Ba, Nd	85	3	8	2

Ceramic substrate : Moving toward functionalization

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Ceramics Al₂O₃ SC with ferroelectric nanolayer

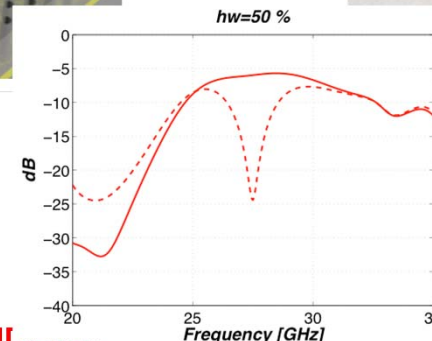
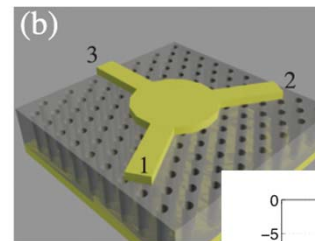
Application toward tuneable capacitors



Tension Vc	Capacité F	Capacité G	Capacité H	Capacité I
0V	8,11	9,97	7,76	9,22
0,5V	6,56	7,84	6,69	7,32
1V	4,94	5,66	4,3	5,35
1,5V	3,79	4,19	3,37	4,01
2V	3,02	3,27	2,73	3,14
2,5V	2,5	2,65	2,27	2,57
3V	2,12	2,23	1,95	2,16
3,5V	1,84	1,92	1,7	
4V	1,62	1,69	1,52	1,867
Ratio=C(0V)/C(4V)	5,01	5,90	5,11	4,94

Ceramics dopped with magnetic nanowire

Application toward integrated isolator



Laminate : reliability (CTE), higher density to thinner layer laminate, flexible

Material (resin)	ϵ_r	$\tan \delta \cdot 10^{-3}$	CTEz ppm/°C	Tg °C	
Modified epoxy	3.5-3.8	8-20	> 40	> 180	Amorphous
Thermoset	3.55-10	3-5	> 40	> 260	
Thermoplastic	2.9-3.2	1-2	20-30	> 280	Crystalline
New : LCP	2.9-3.0	2-3	> 150	> 310	

New sequential multilayer board for microwave : use of LCP

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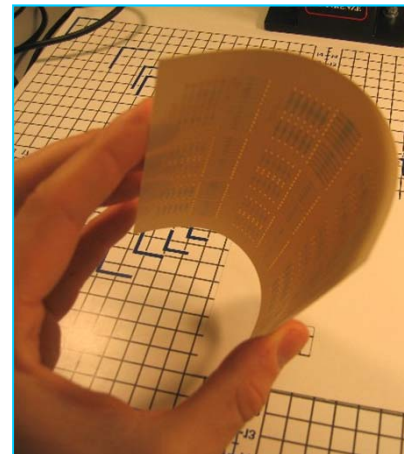
Good and stable dielectric properties

Very good copper adhesion

Possibility to achieve small via diameters, fine tracks / gaps

Possibility to achieve etched resistors both in outer layer and buried in the circuit

Full technological evaluation on going on a 6 layer LCP board according to ECSS standards



19/06/2014

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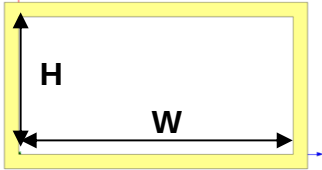
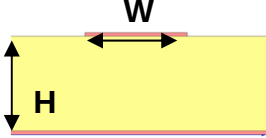
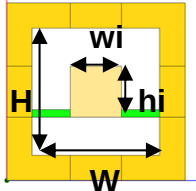
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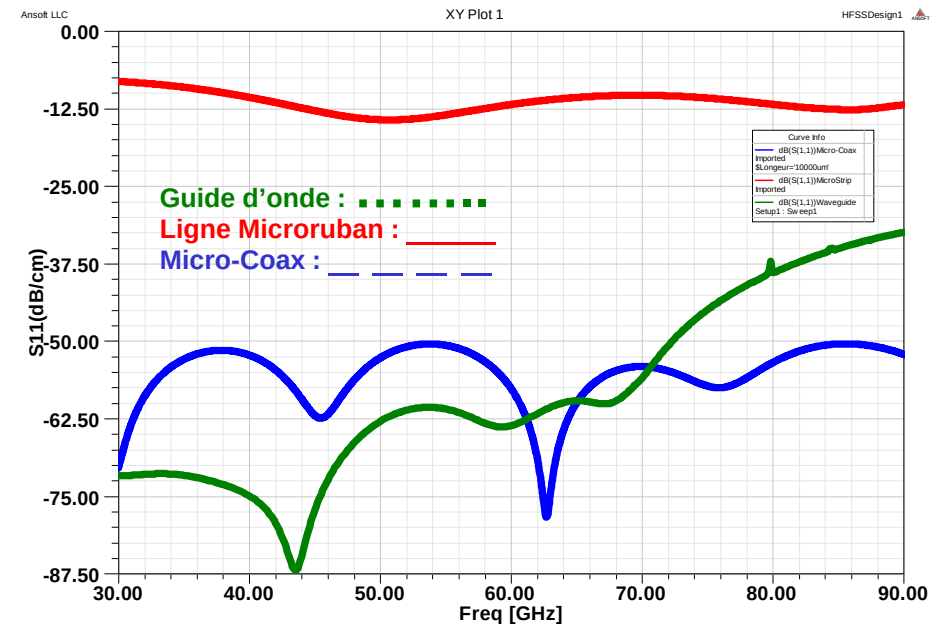
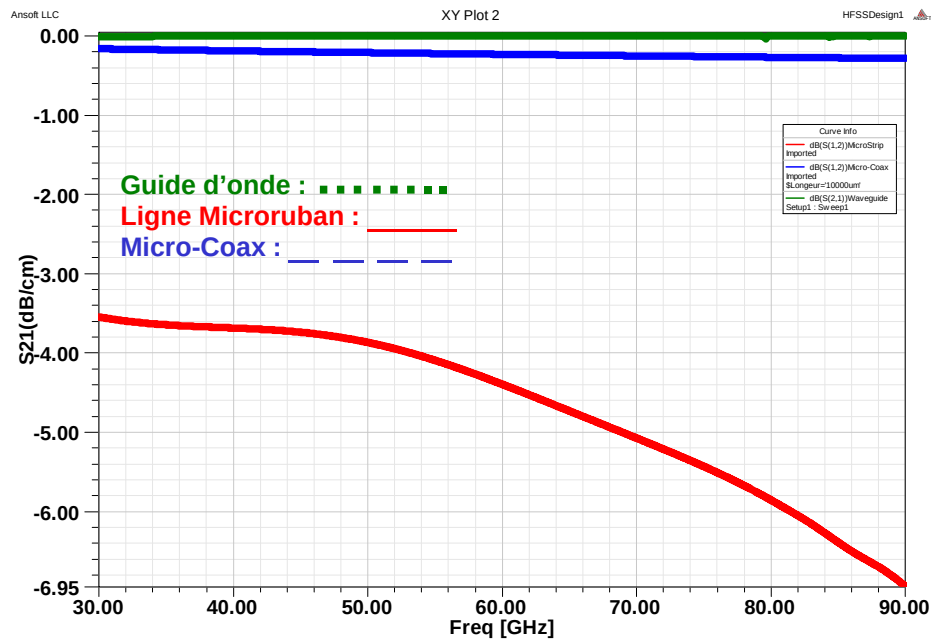
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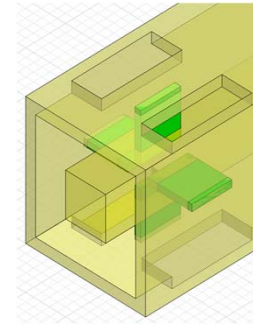
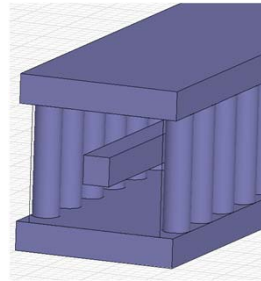
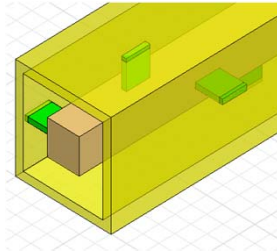
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Study case : Frequencies higher than 30 GHz

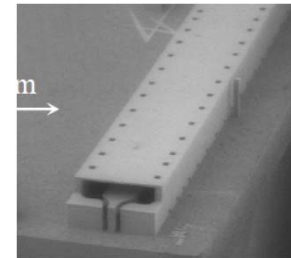
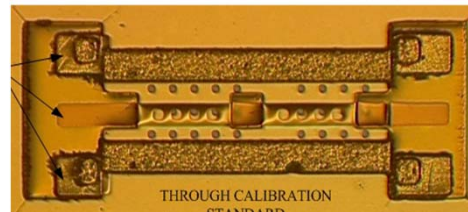
Type :	waveguide 	Microstrip line 	Micro-Coax 
Dimensions	$H=2844\mu\text{m}$ $W=5689\mu\text{m}$	$H=254\mu\text{m}$ $W=144\mu\text{m}$	$H=250\mu\text{m}$ $hi=100\mu\text{m}$ $W=250\mu\text{m}$ $wi=100\mu\text{m}$
Substrat :	Air	Alumine	Air



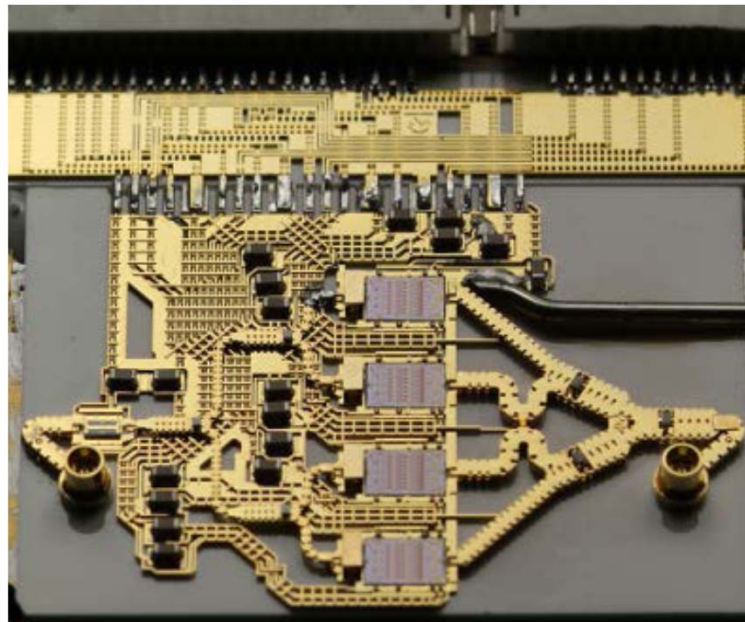
- Design and validation of basic transition and function



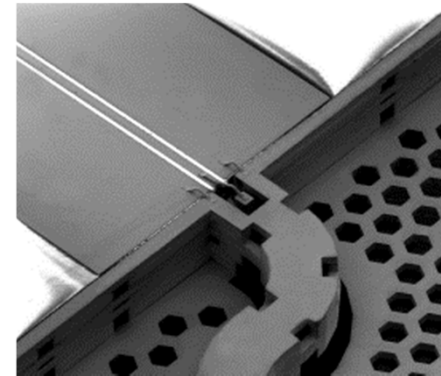
- Analysis of reliability of built structure



Fully integrated subsystem



SSPAs / Power Combining
(courtesy of Nuvotronics)



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On-going microwave packaging activities :

New material engineering and evaluation : substrate, package, assembly ,

- Substrate and material with new properties : composite approach
- New interface material for improved performance
- Multifunctional ceramic substrate to enable integration of passive components (variable capacitor, magnetic, non reciprocal materials)
- Disruptive interconnect using microelectronics techniques

Target ultimate objective:

- Increase of microwave & thermal performances,
- Reduction of mass budget,
- Increase of reliability,

Induce cost reduction at system level