



Photo: ESA



Fiber-Optic Transceivers for Space

Intra-satellite Fibre Optic Links Workshop, at ESTEC, 10 Dec 2015

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We develop new smart technologies, profitable solutions and innovative services. We cooperate with our customers to produce technology for business and build success and well-being for the benefit of society.

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- Turnover 277 M€
 (VTT Group 2014), personnel
 2,600 (VTT Group 1.1.2015)
- Unique research and testing infrastructure
- Wide national and international cooperation network

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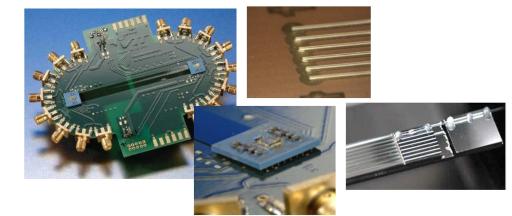


VTT Technologies for Optical Connectivity

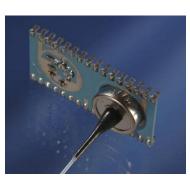


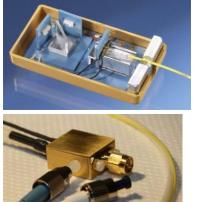


Optical transceivers for harsh environments (aerospace)

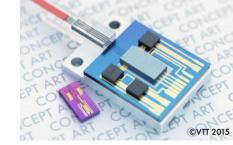


Optical interconnects on printed circuit board, and polymer waveguides

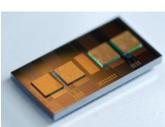


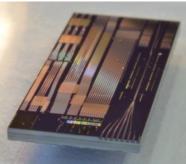


Fiber-optic modules (e.g. for radio-over-fiber) 28/01/2016 - © VTT Ltd









Silicon photonics



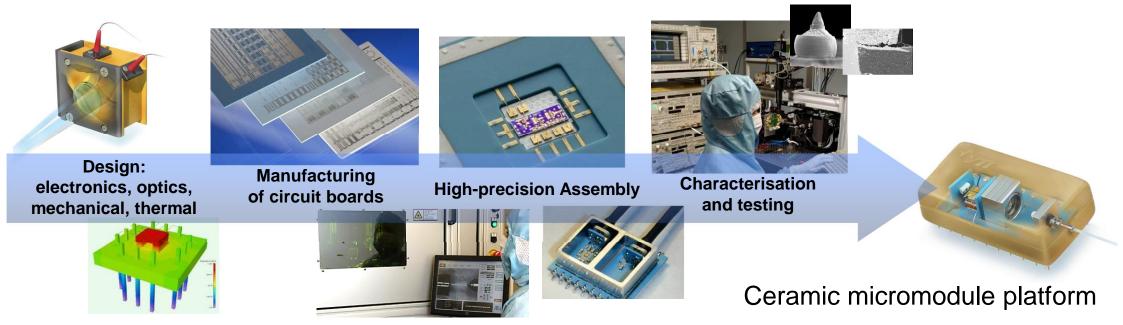
Transceiver technology



Optoelectronics for harsh environments

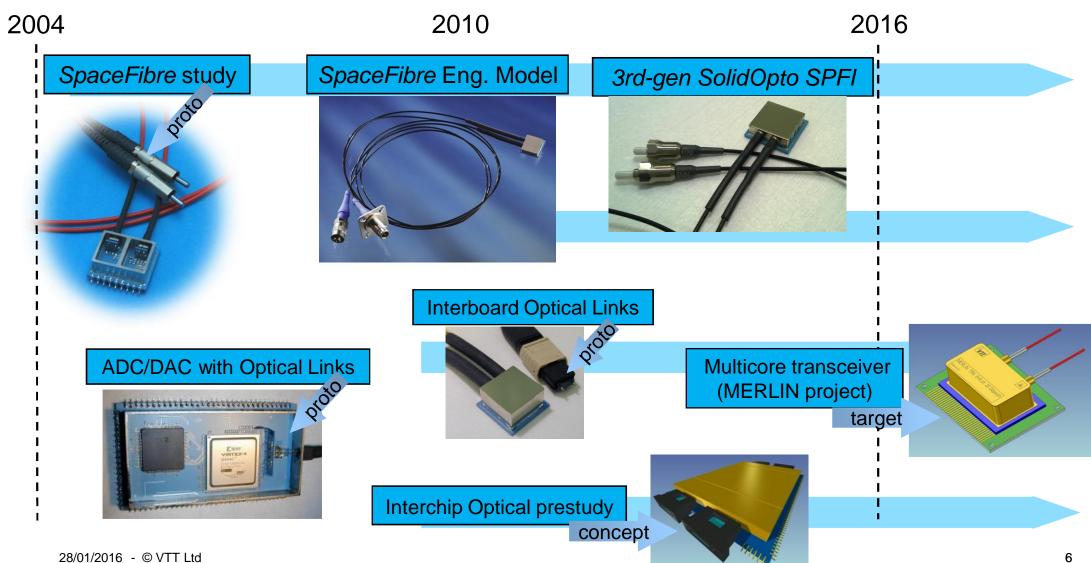
VTT's photonics integration & packaging competences include:

- High-speed electronics integration
- Robust metal-ceramic photonics packaging
- In-house facility for prototyping and small series manufacturing





Overview of VTT's transceiver activities for intra-satellite links





SpaceFibre – High-Speed Fibre-Optic Link

SpaceFibre is a high-speed extension to SpaceWire standard:

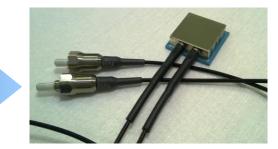
 Objective is to provide symmetrical, full-duplex, point-to-point communication with 1...10 Gbps data rates over 100 m.

VTT has developed transceiver components for SpaceFibre, based on:

- 850-nm VCSEL lasers: Lowest power-per-bit efficiency of the short range links; high integration density; mature; reliable; radiation tolerant
- 50/125 µm silica fiber: enough bandwidth, rather easy optical coupling



Engineering Model of (6.25 Gbps) SpaceFibre transceiver



3rd Gen(6.25 Gbps) SpaceFibre transceiver

1st gen (3 Gbps) SpaceFibre prototype



VTT's SolidOpto SPFI-(003)-6G

- 6.25 Gbps 850-nm Transceiver for Harsh Environment

- Up to 6.25 Gbps full-duplex data link for short range applications
- Protocol independent; but compatible with SpaceFibre physical layer

CML

input

CML

output

VCSEL

driver

TIA & LIA

Transceiver module

VCSEL

Photo

diode

Optical

fibre

Optical

fibre

Data link functional diagram

Photo

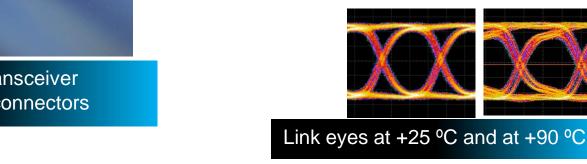
diode

VCSEL

- Power consumption 210 mW (typical)
- 50/125 µm multimode fiber pigtails



SolidOpto SPFI transceiver with Radiall LuxCis connectors



CML

output

CML

input

TIA & LIA

VCSEL

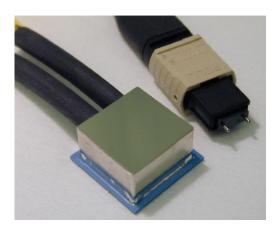
driver

Transceiver module

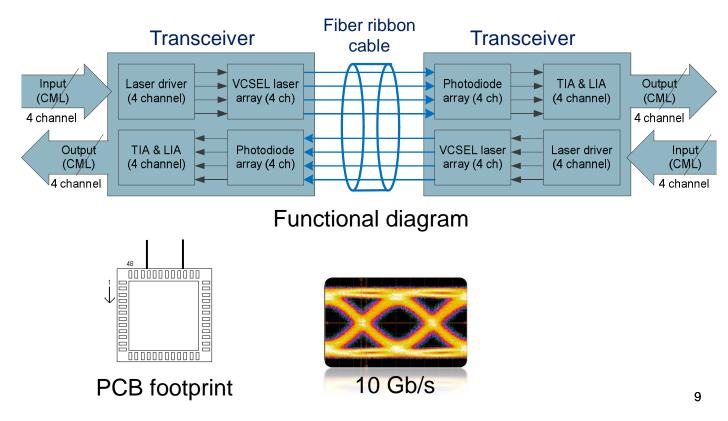


Parallel Optic (4+4 ch) Transceiver – SolidOpto Ol2

- Up to 4x10 Gbps full-duplex data link for short distances
- Protocol independent; e.g. inter-board interconnects in on-board processor
- Power consumption ca 750 mW (=19mW/Gbps)
- Dimensions 17 x 17 x 9 mm³
- Hermetic package with multimode fiber ribbon pigtail



SolidOpto OI2 transceiver with 8-fiber MTP connector

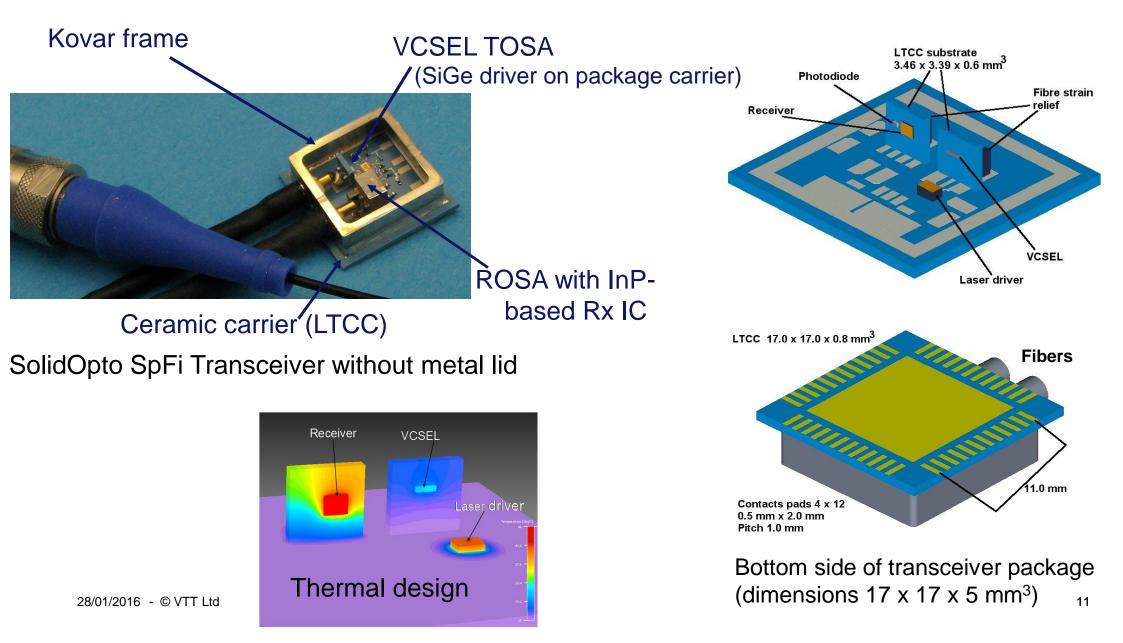




Integration and packaging



6.25 Gbps SpaceFibre Transceiver Structure





Hermetic transceiver packaging and reliability

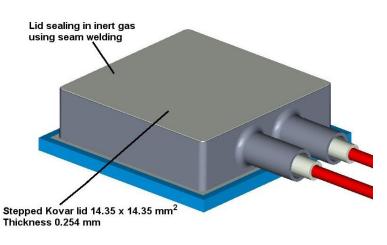
- Kovar frame and lid soldered to ceramic carrier
- Glass-metal fiber feedthrough using solder glass preform

ferrule

Glass preform

Photo by Diemat Corp

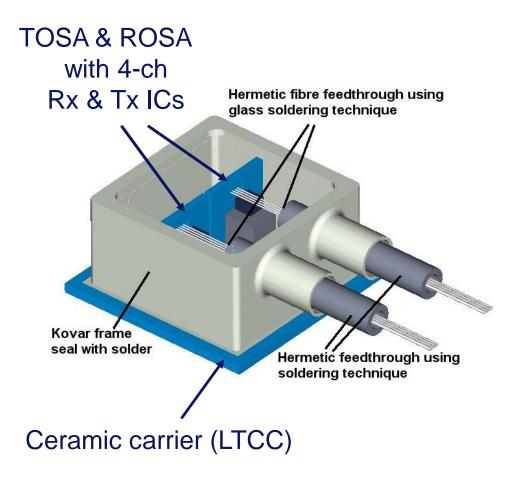
DIEMAT



 Sealed packages passed the helium leak tests after been stressed with temperature cycling -55...+125 °C up to 1000 cycles



Parallel Optic (4+4ch) Transceiver – Packaging





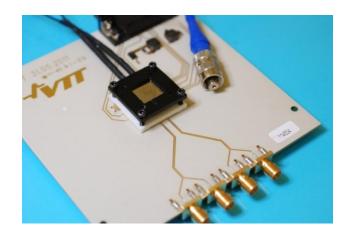
More environmental reliability – *SpaceFibre* TRx

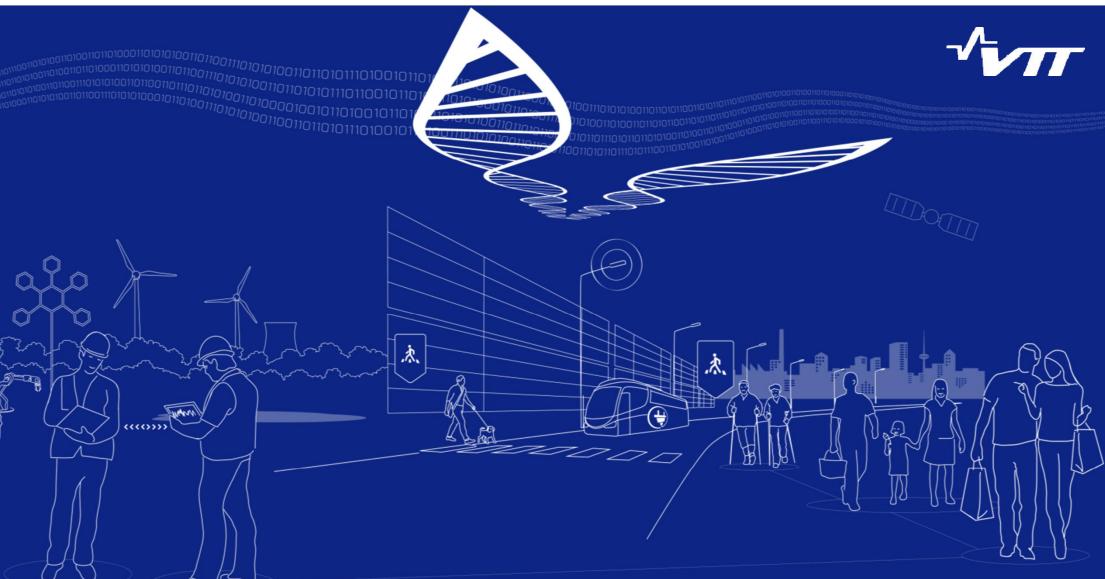
Radiations

- Passed specified total dose tests:
 - Gamma radiation up to 100 krad
 - Proton fluence 10¹² p/cm² (@ 60 MeV)
- Laser drivers used in the EM model showed tendency to latch-up effects at high dose of heavy ions (LET >35 MeV/mg/cm²), however, the laser driver IC has been changed after that.

Mechanical testing

- Vibrations: passed 50 g_{rms}
- Shock: passed 3,000 g
- Thermal vacuum passed -45...+85 °C
- **ESD** Receiver part max ±150 V





Towards next generation



Towards inter-chip optical I/O

- Studied: Interchip-Optical Communications and Photonic PCBs for Next Generation On-Board Processors (ESA TRP, 2008 – 2011)
- Optical interface should be as close as possible to ASIC/FPGA, in order to minimize overall power dissipation and complexity
 - From hybrid integration up to chip-level
 - Proposed 1st generation solution:

Integrate multi-channel fiber-optic I/O into the IC package

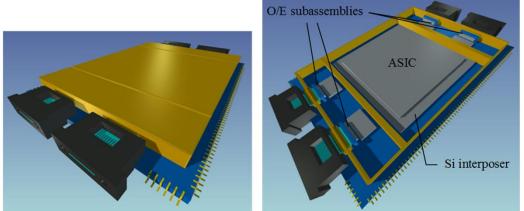
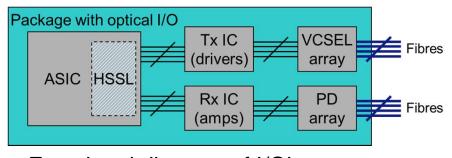


Illustration of space-grade ASIC package with fibre-ribbon I/O's 28/01/2016 - © VTT Ltd



Functional diagram of I/O's



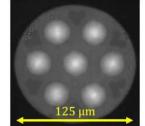




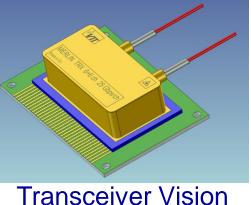
Multi-Gigabit, Scalable & Energy Efficient on-board Digital Processors Employing Multicore, Vertical, Embedded Optoelectronic Engines

Project objectives include:

- Ruggedized optical transceiver "engines" with
 - Record-high data rate of 150 Gb/s, per fiber
 - Very wide temp range, rad-hard, low power
- Very dense integration by the use of:
 - Novel multicore fibers
 - Custom VCSEL & PD arrays
 - Custom multi-channel ASICs



Multicore fiber (©OFS)







Outlook and Challenges



About component selections

- So far used commercial off-the-self photonics and IC devices:
 - Can exploit advances in datacom technology (e.g. power efficiency)
 - Inexpensive compared to custom devices (space is small market)
 - Screening for e.g. radiation, wide temp range, material outgassing
- Mature 850nm GaAs-based VCSEL and PD technology:
 - Low power and good scalability
 - Reliability; also shown good radiation tolerance
- Silica multimode fibers (50/125 µm)
 - Rad-hard fibers available from a few manufacturers
 - More limited availability of suitable aerospace cabling (e.g. by W.L. Gore and by OFS)



Summary

- VTT developed high-bit-rate data links for satellite payloads with transceivers based on:
 - Advanced metal-ceramic photonics packaging
 - 850-nm VCSELs and multimode fibers
- Components developed for intra-satellite communications:
 - "SpaceFibre" fibre-optic transceivers (6.25 Gb/s)
 - Parallel optical transceivers (4+4 x 10 Gb/s)
- Developing higher speed and more inter-chip/package interconnects:
 - Next-gen ASIC packaging with integrated optical I/O
 - High-density, multi-channel, up to 150 Gb/s
- Pursuing full commercialization



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