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RESISTOR, FIXED, CHIP, METAL FOIL

BASED ON TYPE SMP-PW, SMS-PW, SMT-PW

ESCC Detail Specification No. 4001/027

Issue 5	November 2017



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DOCUMENTATION CHANGE NOTICE

(Refer to https://escies.org for ESCC DCR content)

DCR No.	CHANGE DESCRIPTION
1103	Specification upissued to incorporate changes per DCR.



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1 <u>GENERAL</u>

1.1 <u>SCOPE</u>

This specification details the ratings, physical and electrical characteristics and test and inspection data for the component type variants and/or the range of components specified below. It supplements the requirements of, and shall be read in conjunction with, the ESCC Generic Specification listed under Applicable Documents.

1.2 <u>APPLICABLE DOCUMENTS</u>

The following documents form part of this specification and shall be read in conjunction with it:

- (a) ESCC Generic Specification No. 4001.
- (b) A-QS-009, Isabellenhütte Test Procedure for Infrared Measurement.

1.3 TERMS, DEFINITIONS, ABBREVIATIONS, SYMBOLS AND UNITS

For the purpose of this specification, the terms, definitions, abbreviations, symbols and units specified in ESCC Basic Specification No. 21300 shall apply.

1.4 THE ESCC COMPONENT NUMBER AND COMPONENT TYPE VARIANTS

1.4.1 <u>The ESCC Component Number</u> The ESCC Component Number shall be constituted as follows:

Example: 4001027031R5D

- Detail Specification Reference: 4001027
- Component Type Variant Number: 03 (as required)
- Characteristic code: Resistance Value (1.5Ω): 1R5 (as required)
- Characteristic code: Resistance Tolerance (±0.5%): D (as required)

1.4.1.1 Characteristics Codes

Characteristics and/or ratings to be codified as part of the ESCC Component Number shall be as follows:

(a) Resistance Value expressed by means of the following codes. The unit quantity shall be ohm (Ω):

Resistance Value (Ω)	Code
0.00XX	R00XX
0.0XX	R0XX
0.XX	RXX
X.X	XRX

(b) Resistance Tolerance expressed by the following codes in accordance with ESCC Basic Specification No. 21700:

Tolerance (± %)	Code Letter		
0.5	D		
1	F		

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1.4.2 Component Type Variants and Range of Components

The component type variants and range of components applicable to this specification are as follows:

Variant Number	Туре	Style (Note 1)	Resistance Range Rn		Tolerance (± %)	Temperature Coefficient TC	Value Series	Weight max
			Min (Ω)	Max (Ω)		(± 10 ⁻⁶ /°C)		(g)
01	SMP-PW	2010	0.0050	1.0	0.5, 1	Note 2	E12	0.06
02	SMS-PW	2512	0.0030	1.0	0.5, 1	Note 2	E12	0.1
03	SMT-PW	2817	0.0040	2.0	0.5, 1	Note 2	E12	0.15

<u>NOTES</u>

- 1. See Para. 1.6.
- 2. Temperature Coefficient, TC, requirements:

Temperature Coefficient TC (± 10 ^{-6/°} C)	Requirement
-100, +0	over T _{amb} = -55°C to +22°C
±60	over T _{amb} = +22°C to +170°C
±50	over T_{amb} = +22°C to +60°C



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1.5 MAXIMUM RATINGS

The maximum ratings shall not be exceeded at any time during use or storage.

Maximum ratings shall only be exceeded during testing to the extent specified in this specification and when stipulated in Test Methods and Procedures of the ESCC Generic Specification.

Characteristics	Variant Number	Style	Symbols	Limits	Units	Remarks
Rated Dissipation	01 02 03	2010 2512 2817	Pn	1 2 3	W	Note 1 Note 2 Note 1
Rated Voltage	All	All	U _R	√(Pn x Rn)	V	Note 3
Isolation Voltage	All	All	Uı	200	V	-
Operating Temperature Range	All	All	T _{op}	-55 to +170	°C	T _{amb}
Storage Temperature Range	All	All	T _{stg}	-55 to +170	°C	-
Soldering Temperature	All	All	T _{sol}	+260	°C	Note 4

NOTES:

1. At $T_{amb} \leq +130^{\circ}$ C. For $T_{amb} > +130^{\circ}$ C, derate linearly to 0W at $T_{amb} = +170^{\circ}$ C.

2. At $T_{amb} \le +120^{\circ}$ C. For $T_{amb} > +120^{\circ}$ C, derate linearly to 0W at $T_{amb} = +170^{\circ}$ C.

3. R_n = rated resistance.

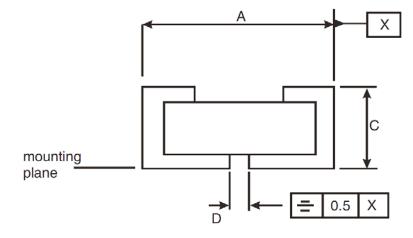
4. Duration 5 seconds maximum.

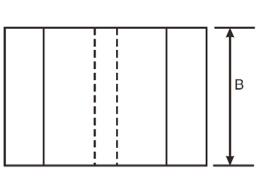


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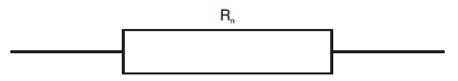
1.6 PHYSICAL DIMENSIONS





Variant Number	Style	Dimensions (mm)							
Number		ļ	A B		С		D		
		Min	Max	Min	Max	Min	Max	Min	Max
01	2010	4.98	5.18	2.44	2.64	0.5	0.9	0.6	1.2
02	2512	6.15	6.55	2.95	3.15	0.6	1	0.6	1.2
03	2817	6.9	7.3	4.1	4.3	0.6	1	0.6	1.2

1.7 FUNCTIONAL DIAGRAM



1.8 MATERIALS AND FINISHES

1.8.1 <u>Terminations</u>

The termination and finish shall be electroplated tin-lead Sn60 to a maximum thickness of 20µm.



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2 <u>REQUIREMENTS</u>

2.1 <u>GENERAL</u>

The complete requirements for procurement of the components specified herein are as stated in this specification and the ESCC Generic Specification. Permitted deviations from the Generic Specification, applicable to this specification only, are listed below.

Permitted deviations from the Generic Specification and this Detail Specification, formally agreed with specific Manufacturers on the basis that the alternative requirements are equivalent to the ESCC requirement and do not affect the component's reliability, are listed in the appendices attached to this specification.

2.1.1 Deviations from the Generic Specification

2.1.1.1 Deviations from Production Control - Chart F2

(a) Infrared Measurement

Add Infrared Measurement in accordance with A-QS-009 prior to Dimension Check. Applied Voltage:

- Variant 01: $\sqrt{(12P_n \times R_n)}$
- Variant 02: $\sqrt{(7.5P_n \times R_n)}$
- Variant 03: $\sqrt{6.7P_n \times R_n}$
- Duration: 10ms

2.1.1.2 Deviations from Screening Tests - Chart F3

- (a) Screening Sub-lot Samples Selection, Mounting and Testing A quantity of 20 components shall be selected at random from the production lot and mounted in accordance with ESCC Generic Specification No. 4001 Para. 8.7. These mounted Screening Sub-lot samples shall be serialised and subjected to tests and inspections of Chart F3 in parallel with the 100% testing. For each test requiring electrical measurements the results shall be recorded against component serial number. These samples shall not form part of the delivery lot.
- (b) Para. 8.1 Overload Overload shall only be performed on the mounted Screening Sub-lot samples. Resistance shall be measured as specified in Para. 2.5.1 Room Temperature Electrical Measurements both before and after Overload. Change in Resistance shall be calculated, recorded against component serial number, and shall not exceed the limit as follows:
 - Change in Resistance during Overload: ±0.3% max.
- (c) Para. 8.2, Non-Linearity: Not applicable.
- (d) Para. 8.3.2, Room Temperature Electrical Measurements

For the testing of the mounted Screening Sub-lot samples, Resistance shall be recorded against component serial number both before and after Burn-in. Change in Resistance shall be calculated, recorded against component serial number, and shall not exceed the limit as follows:

- Change in Resistance during Burn-in: ±0.2% max.
- (e) Para. 8.4 Burn-in For the 100% Testing during Chart F3, burn-in in accordance with Para. 2.7.1 No-Bias Burn-in of this specification shall be performed with a duration of 24 (+24, -0) hours.
 For the testing of the mounted Screening Sub-lot samples, burn-in in accordance with Para. 2.7.2 Biased Burn-in of this specification shall be performed with a duration of 168 (+24 -0) hours.
- (f) Para. 6.4 Check for Lot Failure
 0 failures are allowed during all Chart F3 testing of the mounted Screening Sub-lot samples. In the event of any failure the lot shall be considered as failed.



- 2.1.1.3 Deviations from Qualification and Periodic Tests Chart F4
 - (a) Para. 8.3.1.2, Insulation Resistance: Not applicable.
 - (b) Para. 8.11, Robustness of Terminations: Not applicable.

2.1.1.4 Deviations from Data Documentation

(a) Para. 9.6, Screening Tests Data: For each test performed on the mounted Screening Sublot samples requiring electrical measurements, the results shall be recorded against component serial number. Component drift calculations shall be recorded for each specified test against component serial number.

2.2 <u>MARKING</u>

The marking of all components delivered to this specification shall be in accordance with the requirements of ESCC Basic Specification No. 21700. When the component is too small to accommodate all of the marking specified, as much as space permits shall be marked and the marking information, in full, shall accompany each component in its primary package.

The information to be marked and the order of precedence, shall be as follows:

- (a) The ESCC qualified components symbol (for ESCC qualified components only).
- (b) The ESCC Component Number.
- (c) Traceability information.

2.3 <u>OVERLOAD</u>

The test conditions for Overload, tested as specified in the ESCC Generic Specification, shall be as follows:

- Voltage: $\sqrt{5P_n \times R_n}$
- Duration: 5s.

2.4 RESISTANCE TO SOLDERING HEAT

The test conditions for Resistance to Soldering Heat, tested as specified in the ESCC Generic Specification, shall be as follows:

- Temperature: +260°C
- Duration: 5 (+0 -1)s

2.5 ELECTRICAL MEASUREMENTS AT ROOM, HIGH AND LOW TEMPERATURES

2.5.1 Room Temperature Electrical Measurements

The measurements shall be performed at T_{amb} = +22 ±3°C.

Characteristics	Symbols	ESCC 4001 Test Method and	Tolerance (± %)	Lin	nits	Units
		Conditions	(± /0)	Min	Max	
Resistance	RA	Para. 8.3.1.1	0.5	0.995 R _n	1.005 R _n	Ω
			1	0.99 R _n	1.01 R _n	



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2.5.2 <u>High and Low Temperatures Electrical Measurements</u>

Characteristics	Symbols		SCC 4001	Lin	Units	
		I est Metho	od and Conditions	Min	Max	
Resistance Change between -55 (+3 -0)ºC and +22 ±3 ºC	∆Ra/Ra	Para. 8.3.1.1 Note 1	TC = -100 x 10 ⁻⁶ /°C	-0.8	+0	%
Resistance Change between +22 ±3°C and +170 (+0 -3)°C	∆Ra/Ra	Para. 8.3.1.1 Note 1	TC = ±60 x 10 ⁻⁶ /⁰C	-0.906	+0.906	%
Resistance change between +22 ±3°C and +60 ±3°C	∆Ra/Ra	Para. 8.3.1.1 Note 2	TC = ±50 x 10 ⁻⁶ /°C	-0.22	+0.22	%

NOTES:

- 1. The measurements shall be performed on all the mounted Screening Sub-lot samples.
- 2. The measurements shall be performed on a sample of 5 components selected from the total production lot.

2.6 INTERMEDIATE AND END-POINT ELECTRICAL MEASUREMENTS

The components shall be mounted as specified in the ESCC Generic Specification.

Unless otherwise specified, the measurements shall be performed at T_{amb} = +22 ±3°C.

Unless otherwise specified the test methods and test conditions shall be as per the corresponding test defined in Para. 2.5.1 Room Temperature Electrical Measurements.

Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 4001			Min	Max	
Rapid Change of Temperature					
Initial Measurement	Resistance	RA	Record Values		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.2		%
Vibration					
Initial Measurement	Resistance	R _A	Record Values		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.2		%
Resistance to Soldering Heat					
Initial Measurement	Resistance	RA	Record Values		
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±0.1		%
Solderability					
Initial Measurement	Resistance	RA	Record	Values	
Final Measurement	Change in Resistance	$\Delta R_A/R_A$	±C).1	%



Test Reference per	Characteristics	Symbols	Limits		Units
ESCC No. 4001			Min	Max	
Climatic Sequence					
Initial Measurements (Procedure 1)	Resistance (after drying)	RA	Record Values		
Final Measurements	Change in Resistance	$\Delta R_A/R_A$	±0.3		%
	Insulation Resistance	-	Not applicable		-
Operating Life					
Initial Measurement (0 hour)	Resistance	RA	Record Values		
Intermediate Measurements (1000 hours)	Change in Resistance	$\Delta R_{A}/R_{A}$	±0.2		%
Final Measurements	Change in Resistance	$\Delta R_A/R_A$	±C).2	%
(2000 hours)	Insulation Resistance	-	Not ap	olicable	-

2.7 BURN-IN CONDITIONS

2.7.1 No-Bias Burn-in Conditions for 100% Testing

Characteristics	Symbols	Conditions (Note 1)	Units
Ambient Temperature	T _{amb}	+170 (+0 -5)	°C
Test Voltage	VT	No Bias	V

NOTES:

- 1. No-Bias Burn-in (High Temperature Bake) shall be performed on components that are not mounted.
- 2. No-Bias Burn-in duration shall be 24 (+24 -0) hours.
- 3. After Burn-in, the components shall be removed from the chamber and allowed to cool under normal atmospheric conditions for a minimum of 1 hour. They shall then be visually examined. There shall be no evidence of damage and marking shall still be legible.

2.7.2 Biased Burn-in Conditions for Screening Sub-lot Samples

Characteristics	Symbols	Conditions (Note 1)	Units
Terminal Temperature Variants 01, 03: Variant 02:	T _{term}	+130 (+0 -3) +120 (+0 -3)	°C
Test Voltage	Vτ	√(Pn x Rn)	V

NOTES:

- 1. Biased Burn-in shall be performed on the applicable sample components that are mounted in accordance with ESCC Generic Specification No. 4001 Para. 8.7.
- 2. Biased Burn-in duration shall be 168 (+24 -0) hours.
- 3. After Burn-in, the components shall be removed from the chamber and allowed to cool under normal atmospheric conditions for a minimum of 1 hour. They shall then be visually examined. There shall be no evidence of damage and marking shall still be legible.

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2.8 <u>OPERATING LIFE CONDITIONS</u> The conditions shall be as specified in Para. 2.7.2 for Biased Burn-in. ESCC Detail Specification



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<u>APPENDIX A</u>

AGREED DEVIATIONS FOR ISABELLENHÜTTE (D)

Items Affected	Description of Deviations		
Para. 2.1.1.1 Deviations from Generic Specification: Production Control - Chart F2	Para. 5.2.1, Dimension Check: a 100% inspection may be performed.		
Para. 2.1.1.3 Deviations from Generic Specification: Qualification and Periodic Tests - Chart F4	Para. 8.3.1.3, Voltage Proof: Not applicable. Para. 8.10, Climatic Sequence: Para. 8.10.5, Low Air Pressure: Not applicable Para. 8.10.7, DC Load: Not applicable Para. 8.15, Permanence of Marking: Not applicable		
Para. 1.4.1.1 Characteristics Codes: Resistance Value	Peristics Resistance Value may be expressed by means of the following codes the ESCC Component Number. The unit quantity shall be ohm (Ω):		
	Resistance Value (Ω)	Code	
	0.00XX	R00XX	
	0.0XX	R0XX	
	0.XXX	RXXX	
	X.XX	XRXX	
Para. 1.4.2 Component Type Variants and Range of Components	For all Variants additional resistance values, Rn, from within the specified Resistance Range, but outside of the specified Value Series, are available upon request from Isabellenhütte.		
Para. 2.2 Marking: Marking of Tolerance on Resistance Value	The tolerance on resistance value may be marked using the actual numeric value (0.5% or 1%) instead of the specified code letter.		
Para. 2.2 Marking: Marking of Traceability Information	 The manufacturing date code may be marked using a three-digit code where: the first digit is a letter to indicate the year of manufacture in accordance with the 20-year cycle code per Table 10a of IEC Publication No. 60062 the last two digits indicate the week of the year (i.e. 01 to 52) during which encapsulation or the final production process occurred. For example: W41 for week 41 of 2008 		