

		<b>APPLICATION FOR EXTENSION OF ESCC QUALIFICATION APPROVAL</b>				Page 1 Appl. No. 2450 <b>4</b>	
Component Title: TRANSISTORS, MICROWAVE, SMALL SIGNAL, SILICON, BIPOLAR, BASED ON TYPES BFY405, BFY420 AND BFY450		Executive Member: DLR		Date: 23/10/2018			
Components (including series and families) submitted for Extension of Qualification Approval:							1
ESCC COMPONENT NO.	VARIANTS	RANGE OF COMPONENTS	BASED ON	TEST VEHICLE / S	COMPONENT SIMILAR		
5611/008	01 to 03		BFY405 BFY420 BFY450	BFY640-04(ES) <i>(see MoQ ext. 322c)</i>	CFY67		
Component Manufacturer Infineon Technologies AG		2	Location of Manufacturing Plant(s) Am Campeon 1-12 D- 85579 Neubiberg Germany		3	Date of original qualification approval: Date: 01/06/1997  Certificate Ref No. 245	
ESCC Specifications used for Maintenance of qualification testing: Generic: 5010 Issue: 02  Detail(s): 5611/009 Issue: 02 5613/004 Issue: 02		5	Deviations to LVT testing and Detail Specification used: No <input checked="" type="checkbox"/> Yes <input checked="" type="checkbox"/> (supply details in Box 15)  Deviation from current Specifications: No <input checked="" type="checkbox"/> Yes <input checked="" type="checkbox"/> (Supply details)		6	Qualification Extension Report reference and date: 1814LR10, Iss 1, April 2018 1814LR11, Iss 1, April 2018	
Summary of procurement or equivalent test results during current validity period in support of this application (those to ESCC listed first)							
Project Name	Testing Level	LAT	Date code	Quantity Delivered			
PID changes since last MoQ  None <input type="checkbox"/> Minor* <input checked="" type="checkbox"/> Major* <input type="checkbox"/>		9	Current PID Verified by:  Ref No:      Generic PID: Detail PID: Issue:      See above Rev Date: 26/10/2018		B. Gökgöz  Name of Executive Representative A63500-GEPIID-P000, Issue 2c, 18.10.2018 A63500-T1503-P000, Issue 3a, 18.10.2018 Date: See above		
*Provide details in box: See Detail PID							
Current Manufacturing facilities surveyed by: T. Kaupisch on 17-18/10/2018 (Name of Executive Representative) (Date)							
Satisfactory: Yes <input checked="" type="checkbox"/> No <input type="checkbox"/> Explain							
Report Reference: IFX-AUD-2018							



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Failure Analysis, DPA, NCCS available:

Yes



No



(Supply data)

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Ref. No's and purposes:

The undersigned hereby certifies on behalf of the ESCC Executive - that the above information is correct; -  
that the appropriate documentation has been evaluated; - that full compliance to all ESCC requirements is evidence  
(except as stated in box 15;) - that the reports and data are available at the ESCC Executive and therefore applies on behalf of  
DLR as the responsible Executive Member for ESCC qualification status to be extended to the component(s) listed herein.

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Date: 23/10/2018

B. Gökğöz

(Signature of the Executive Coordinator)

Continuation of Boxes above:

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Non compliance to ESCC requirements:				15
No.:	Specification	Paragraph	Non compliance	
1	ESCC 5010	CHART F4	No periodic repetition of Endurance Subgroup	
2	ESCC 5010 ISSUE 3 (APRIL 2017)		ISSUE 2 USED FOR MAINTENANCE OF QUALIFICATION.	
Additional tasks required to achieve full compliance for ESCC qualification or rationale for acceptability of noncompliance:				16
The approved Detail PIDs describe the agreed methodology for maintenance of qualification regarding similarity in § 7  <div style="text-align: center; font-size: 1.2em; margin-top: 20px;">           REVISED PID APPROVED IN OCT. 2018         </div>				
Executive Manager Disposition				17
Application Approval:    Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>				
Action / Remarks:				
<div style="text-align: right; margin-right: 100px;">         (B. SCHADE)     </div>				
Date:				
Signature of ESA Head of Product Assurance and Safety Department				

\* LAT2 on actual wafer was performed satisfactorily on Date Code: 0007L and 0714A



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Chart V	Test	Tick when done	Conditions	Date Code Diffusion Lot	Tested Qty	No. of Rejects	Comments if not performed. Comments on Rejection
Assembly Capability Tests	Solderability Test	<input checked="" type="checkbox"/>	MIL-STD-750 Test Method 2026	1632A	2 4	0 0	
	Permanence of Marking	<input checked="" type="checkbox"/>	ESCC Basic Specification No. 24800	1632A	2 4	0 0	
	Terminal Strength	<input checked="" type="checkbox"/>	MIL-STD-750 Test Method 2036	1632A	2 4	0 0	
De-encapsulation Tests	Internal visual inspection	<input checked="" type="checkbox"/>	ESCC Basic Specification No. 20400	1632A 1709A	2 6	0 0	
	Bond Strength	<input checked="" type="checkbox"/>	MIL-STD-750 Test Method 2037	1632A 1709A	2 6	0 0	
	Die Shear	<input checked="" type="checkbox"/>	MIL-STD-750 Test Method 2017	1632A 1709A	2 6	0 0	



