







Advanced Electronic Microsystems Excellence in (opto) electronics and sensor systems offering high-end chip level technology services from product / process development to packaging





Technology-Know-How For complex module assembly

Wafer Back-End	Flip Chip	Chip on Board	SMT	Box-Build
 Au-Stud Bumping UBM Solder Balling Sawing Cleaning and UV-Exposure 	 Soldering Gluing (ICA, ACA, NCA) Copper Pillar TC/TS Bonding Underfill 	 Die Bonding Al and Au Wedge and Ball Bonding Encapsulation 2.5D/3D Packaging System in Package 	 01005 and 03015 (metric) Selective Soldering RoHS-Compliant Processes 	 Co-Product Development Prototyping + Industrialization Serial Production incl. Repair Service Worldwide Supply

On all common substrates



Description of the projects

TWO STAGES
FROM WAFER TO CHIP
QUALIFIED NON-HERMETIC FC





FROM WAFER TO CHIP



- ✓ Faster time to market
- ✓ One face to the customer
- ✓ Fast root cause analysis

- ✓ Streamlined supply chain
- ✓ Full transparency and control of the supply chain
- ✓ No overhead accumulation



Key parameters in the Wafer Back-End Services



All necessary processes (except for laser grooving) are already qualified in house. We aim to establish through empirical methods the best suitable combination of parameters for all processes in order to achive the qualification for space applications













Non-Hermetic FC – Variables in the qualification

- Number of contacts (Balls)
- Material of the balls
- Size of the balls
- Pitch
- Pad shape and size on substrate
- Substrate
- Underfill

>8000

<100µm <200µm 75x75µm Ceramic degasing





Conclusions

- Highest reliability electronics CAN be assembled in a cost effective manner
- Qualification of the processes implies a long and tedious work
- High level of automation and process control are the key
- The right selection of parameters including interactions between processes needs to be done empirically



Thank You Your Strategic Technology Partner



