

INTEGRATEC CIRCUITS, SILICON MONOLITHIC, CMOS GATE/EMBEDDED ARRAY BASED ON TYPE MH1RT				<b>278rev2</b>
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic ESCC 9000 Detail ESCC 9202/076	<a href="#">Microchip Technology</a> Nantes France	Qualification	ESA/ESTEC	Dec 2006
Remarks				

INTEGRATED CIRCUITS, SILICON MONOLITHIC, CMOS, CELL-BASED ARRAY, BASED ON TYPE ATC18RHA ASIC FAMILY				<b>357</b>
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic ESCC <a href="#">9000</a> Detail ESCC <a href="#">9202/080</a>	<a href="#">Microchip Technology</a> <a href="#">Nantes</a> <a href="#">France</a>	Qualification	CNES	Apr 2019
The Technology Flow is described into the current QML document ( <a href="#">REP006</a> ).				
<a href="#">9202/080</a>	Integrated circuits, silicon monolithic, CMOS, cell-based array	Based on type ATC18RHA		

INTEGRATED CIRCUITS, CMOS, CELL-BASED ARRAY, BASED ON ATMX150RHA ASIC FAMILY				<b>359</b>
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic ESCC <a href="#">9000</a> Detail ESCC <a href="#">9202/083</a>	<a href="#">Microchip Technology</a> Nantes France	Qualification	ESA/ESTEC	Apr 2019
Remarks				
The Technology Flow is described into the current QML document ( <a href="#">REP006</a> ).				
<a href="#">9202/083</a>	Integrated circuits, CMOS, cell-based array.  Ph2, Digital only, up to 22Mgates, 5ML+ thick metal layer.	Based on type ATC18RHA		