

		APPLICATION FOR EXTENSION OF ESCC QUALIFICATION APPROVAL				Page 1 Appl. No. 322D
		Component Title: TRANSISTORS, MICROWAVE, SMALL SIGNAL, SILICON, BIPOLAR, BASED ON TYPES BFY405, BFY420 AND BFY450				
		Executive Member: DLR		Date: 10/11/2019		

Components (including series and families) submitted for Extension of Qualification Approval:						1
ESCC COMPONENT NO.	VARIANTS	RANGE OF COMPONENTS	BASED ON	TEST VEHICLE / S	COMPONENT SIMILAR	
5611/009	01 to 03		BFY 640			
5611/010	01 to 04		BFY 640B, 650B	BFY193C(ES)	Y	
5611/011	01		BFY 740B			

Component Manufacturer Infineon Technologies AG	2	Location of Manufacturing Plant(s) Am Campeon 1-12 D- 85579 Neubiberg Germany	3	Date of original qualification approval: Date: 01/09/2012 Certificate Ref No. 322	4
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ESCC Specifications used for Maintenance of qualification testing: Generic: 5010 Issue: 03 Detail(s): 5512/020 Issue: 20	5	Deviations to LVT testing and Detail Specification used: No ☒ Yes ☐ (supply details in Box 15) Deviation from current Specifications: No ☒ Yes ☐ (Supply details)	6	Qualification Extension Report reference and date: 1922LR10, Iss. 1, Sep 2019	7
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Summary of procurement or equivalent test results during current validity period in support of this application (those to ESCC listed first)					8
Project Name	Testing Level	LAT	Date code	Quantity Delivered	

PID changes since last MoQ None ☐ Minor* ☒ Major* ☐	9	Current PID Verified by: Burak Gökgöz Name of Executive Representative Generic PID: A63500-GEPIID-P000, Issue 2d, 25.09.2019 Detail PID, BFY640: A63500-T580-P000, Issue 4, 16.10.2019 Rest: A63500-T1580B-P000, Issue 4, 16.10.2019	10
*Provide details in box: Gen. PID: 650V Power MOSFET device is implemented Det. PID: updated based on the agreement during ESCC Executive Meeting on 10.10.2019			

Current Manufacturing facilities surveyed by: Thilo Kaupisch on 17-18/10/2018 (Name of Executive Representative) (Date)		11
Satisfactory: Yes ☒ No ☐ Explain		
Report Reference: IFX-AUD-2018		

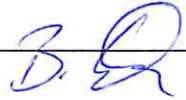
	APPLICATION FOR EXTENSION OF ESCC QUALIFICATION APPROVAL Component title: TRANSISTORS, MICROWAVE, SMALL SIGNAL, SILICON, BIPOLAR, BASED ON TYPES BFY405, BFY420 AND BFY450 Executive Member: DLR Date: 10/11/2019	Page 2 Appl. No. 322D
Failure Analysis, DPA, NCCS available: Yes ☐ No ☒ (Supply data) Ref. No's and purposes:		12
The undersigned hereby certifies on behalf of the ESCC Executive - that the above information is correct; - that the appropriate documentation has been evaluated; - that full compliance to all ESCC requirements is evidence (except as stated in box 15;) - that the reports and data are available at the ESCC Executive and therefore applies on behalf of DLR as the responsible Executive Member for ESCC qualification status to be extended to the component(s) listed herein. Date: 10/11/2019  Burak Gökğöz (Signature of the Executive Coordinator)		13
Continuation of Boxes above:		14

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Non compliance to ESCC requirements:				15
No.:	Specification	Paragraph	Non compliance	

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Executive Manager Disposition		17
Application Approval: Yes ☒ No ☐		
Action / Remarks:		

Date: 27.01.2020	 B. Schade, Head of ESA Product Assurance and Safety Department
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ANNEX 1: LIST OF TESTS DONE TO SUPPORT EXTENSION OF QUALIFICATION

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Tests conducted in compliance with:

- ESCC 5010 generic specification; Chart F4 (for ESCC/QPL parts);

Tests vehicle identification/description:

1922LR10	BFY193C(ES)

Chart F4A	Test	Tick when done	Conditions	Date Code Diffusion Lot	Tested Qty	No. of Rejects	Comments if not performed. Comments on Rejection
Environmental/Mechanical Subgroups	Thermal Shock Test	☒	ESCC 5010 Para. 9.5.2	1848A	8	0	
	Shock Test	☐	MIL-STD-750 Test Method 2016				n.a. acc. Detail Spec
	Vibration Test	☐	MIL-STD-750 Test Method 2056				n.a. acc. Detail Spec
	Constant Acceleration	☐	MIL-STD-750 Test Method 2006				n.a. acc. Detail Spec
	Seal Test	☐	MIL-STD-750 Test Method 1071				n.a. acc. Detail Spec
	Moisture Resistance	☒	MIL-STD-750 Test Method 1021	1848A	8	0	
	Seal Test	☒	MIL-STD-750 Test Method 1071	1848A	8	0	
	Electrical Measurements at Room Temp.	☒	Table 2 of the Detail Specification	1848A	8	0	
	External Visual Inspection	☒	ESCC Basic Specification No. 20500	1848A	8	0	
Endurance Subgroup*	Operating Life	☐	MIL-STD-750 Test Method 1026				Former data from Wafer available
	Electrical Measurements during Endur. Test	☐	Table 6 of the Detail Specification				Former data from Wafer available
	External Visual Inspection	☐	ESCC Basic Specification No. 20500				Former data from Wafer available

* LAT2 on actual wafer was performed satisfactorily used in:

Assembly Lot:

1008.02

0715.02

Date Code:

1016A

0716B

Chart F4B	Test	Tick when done	Conditions	Date Code Diffusion Lot	Tested Qty	No. of Rejects	Comments if not performed. Comments on Rejection
Electrical Subgroup – Electrical Measurements	Electrical Measurements at Room Temp.	☐	Table 2 of the Detail Specification				
	Electrical Measurements at High & Low Temp's	☐	Table 3 of the Detail Specification				
	External Visual Inspection	☐	ESCC Basic Specification No. 20500				
	Special Testing	☐	The Detail Specification				n.a. acc. Detail Spec
Electrical Subgroup – Assembly – Capability Tests	Solderability Test	☒	MIL-STD-750 Test Method 2026	1848A	4	0	
	Permanence of Marking	☐	ESCC Basic Specification No. 24800				n.a. due to laser marking
	Terminal Strength	☒	MIL-STD-750 Test Method 2036	1848A	4	0	
De-encapsulation Tests	Internal visual inspection	☒	ESCC Basic Specification No. 20400	1848A	6	0	
	Bond Strength	☒	MIL-STD-750 Test Method 2037	1848A	6	0	
	Die Shear	☒	MIL-STD-750 Test Method 2017	1848A	6	0	

