

INTEGRATED CIRCUITS, SILICON, MONOLITHIC, 35KLUT RADIATION-HARDENED FPGA (NG-MEDIUM)				382																		
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date																		
Generic ESCC 9000	NanoXplore France	Qualification	ESA/ESTEC	Aug 2022																		
Detail ESCC 9304/010 9202/086		Remarks																				
<p>Qualified range:</p> <table border="1"> <thead> <tr> <th>Detail spec</th> <th>Variant Number</th> <th>Based on Type</th> <th>Case</th> <th>Terminal Material and Finish</th> <th>Weight max g</th> </tr> </thead> <tbody> <tr> <td>9304/010</td> <td>01</td> <td>NX1H35AS</td> <td>CQFP-352</td> <td>D2</td> <td>28.6</td> </tr> <tr> <td>9304/010</td> <td>02</td> <td>NX1H35AS</td> <td>CLGA-625</td> <td>D2</td> <td>8.8</td> </tr> </tbody> </table> <p>Based on STM C65SPACE ASIC platform technology. Qualified domain is listed in QML Technology Description in REP006.</p> <p>Operating Temperature Range (°C): -55 to +125</p>					Detail spec	Variant Number	Based on Type	Case	Terminal Material and Finish	Weight max g	9304/010	01	NX1H35AS	CQFP-352	D2	28.6	9304/010	02	NX1H35AS	CLGA-625	D2	8.8
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