

## ESCC/RP/QPL005

SILI	INTEGRATED CON MONOLITHIC, CMC BASED ON TYPE ATC	357B		
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic		Qualification	CNES	Apr 2019
ESCC 9000				
Detail ESCC	Microchip Technology			
9202/080,	Nantes			
9512/004, 9304/009,	France			
9512/005				

The Technology Flow is described into the current QML document (REP006).

based array
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## Available standard components:

9512/004	Integrated Circuits, Silicon, 32-bit SPARC Processor	Based on type AT697F
9304/009	Integrated Circuits, Silicon, monolithic, CMOS digital, Field Programmable Gate Array, 280000 gates	Based on type ATF280F
9512/005	Integrated Circuits, Silicon, monolithic, SPARC V8 GNSS Controller	Based on type AT991

ISSUE 244





INTEGRATED CIRCUITS, CMOS, CELL-BASED ARRAY, BASED ON ATMX150RHA ASIC FAMILY				359B
Procurement Specifications	Manufacturer	Nature of Approval	Initial Qualification Date	
Generic		Qualification	ESA/ESTEC	Apr 2019
ESCC 9000	Microchip Technology			
Detail ESCC 9202/083	Nantes France	Remarks		

The Technology Flow is described into the current QML document (REP006).

The qualified range includes variants 75 to 164 from ESCC 9202/083 (Flat-Substrate package).

	Integrated circuits, CMOS, cell-based array.	
9202/083	Ph2, Digital only, up to 22Mgates, 5ML+ thick metal layer.	Based on type ATMX150RHA

INTEGRATED CIRCUITS, SILICON, MONOLITHIC, RADIATION- HARDENED 32-BIT ARM CORTEX-M7 MICROCONTROLLER				372
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic		Qualification	ESA/ESTEC	May 2021
ESCC 9000	Microchip Technology			
Detail ESCC 9512/006	Nantes France	Remarks		

## Qualified range:

Detail spec	Variant Number	Based on Type	Case	Terminal Material and Finish	Weight max g	Total Dose Radiation Level Letter
9512/006	01	SAMRH71	CQFP-256	D2	18	R [100krad(Si)]

Operating Temperature Range (°C): -55 to +125