

5.2.4 United Monolithic Semiconductors, France : GaN HEMT

5.2.4.1 *Contact Information*

| Address                                                                                                                               | ESCC Chief Inspector                                                                                                                |
|---------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------|
| United Monolithic Semiconductors<br>Batiment Charmille<br>Mosaic parc Courtaboeuf<br>10 avenue du Quebec<br>91140 Villebon sur Yvette | Mr B. LAMBERT<br>+33 6 45 74 51 75<br>+33 1 69 86 32 88<br><a href="mailto:Benoit.Lambert@ums-rf.com">Benoit.Lambert@ums-rf.com</a> |

5.2.4.2 *Qualification*


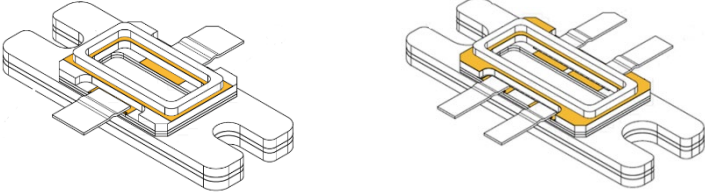
| Current Qualification Certificate No. | In QML since: | Type Designation                                                                               |
|---------------------------------------|---------------|------------------------------------------------------------------------------------------------|
| 388                                   | Mar 2024      | Transistors, Microwave, GaN HEMT Unmatched in Metal Ceramic Package CHKxxxx-SYx Product Family |

5.2.4.3 *List of Qualified Components*


| Variant number | Detail Specification | Generic part number | Component type | Package                        |
|----------------|----------------------|---------------------|----------------|--------------------------------|
| 01             | 5614/009             | CHK8101-SYC         | 15W @ 1.7GHz   | Ceramic-Metal, Flanged, Type C |
| 02             | 5614/009             | CHK8201-SYA         | 50W @ 1.7GHz   | Ceramic-Metal, Flanged, Type A |
| 03             | 5614/009             | CHKA012bSYA         | 130W @ 1.7GHz  | Ceramic-Metal, Flanged, Type A |

5.2.4.4 *Technology Flow Abstract*

CHKxxxx-SYx Product Family is based on the UMS GH50-20 GaN power bar technology. Power bars are assembled using AuSn eutectic soldering in a highly dissipated ceramic-metal Flanged packaged. To assure RF stability of each component type, an RF absorber is used in the package cavity. All manufacturing and test production is performed in accordance to the ESCC/5010.

|                                                                |                                                                                      |          |
|----------------------------------------------------------------|--------------------------------------------------------------------------------------|----------|
| <b>CAPABILITY APPROVAL DOMAIN</b>                              |                                                                                      |          |
| <b>RIC1 (Product: CHK8101-SYC) RIC3 (Product: CHKA012bSYA)</b> |                                                                                      |          |
| <b>GaN DIE PROCESS</b>                                         |                                                                                      |          |
| Die overview                                                   |    |          |
| Manufacturer                                                   | UMS – Ulm – Germany                                                                  |          |
| Power bar dies technology                                      | UMS 4GH50-20                                                                         |          |
| Gate pitches                                                   | 70µm                                                                                 |          |
| Unitary gate finger width                                      | 250µm & 400µm                                                                        |          |
| Finger gates per block                                         | 8                                                                                    |          |
| Number of blocks                                               | 2 , 4, 8                                                                             |          |
| Max operating Freq. (GHz)                                      | 6 GHz                                                                                |          |
| <b>PACKAGE TECHNOLOGY</b>                                      |                                                                                      |          |
| Package overview (without Lid)                                 |  |          |
| Package manufacturer                                           | Kyocera – Gamo – Japan                                                               |          |
| UMS Package reference                                          | SYC SYA                                                                              |          |
| UMS SAP Material N°                                            | 47003170                                                                             | 47002520 |
| Sealing                                                        | Seam-welding process                                                                 |          |

|                                              |                                                                                                                                                                                                                                                                         |                       |
|----------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|
| Lead                                         | 2                                                                                                                                                                                                                                                                       | 4                     |
| Base plate length (mm)                       | 17.5                                                                                                                                                                                                                                                                    | 20                    |
| Base plate width (mm)                        | 6.7                                                                                                                                                                                                                                                                     |                       |
| <u>Lid for Package:</u><br>Manufacturer      | Hi-REL Group                                                                                                                                                                                                                                                            |                       |
| UMS Drawing Document N°                      | 61504640                                                                                                                                                                                                                                                                | 61503116 last version |
| <u>RF Absorber :</u><br>Manufacturer         | DEMGY                                                                                                                                                                                                                                                                   |                       |
| UMS Material N°                              | 47003186                                                                                                                                                                                                                                                                | 47003186              |
| <b>ASSEMBLY TECHNOLOGY</b>                   |                                                                                                                                                                                                                                                                         |                       |
| Assembly subcontractor                       | RHe Microsystems GmbH – Radeberg – Germany                                                                                                                                                                                                                              |                       |
| Preform characteristics                      | Material: Eutectic Au80Sn20 (Liquidis:278°C),                                                                                                                                                                                                                           |                       |
| Sealing process                              | Hermetical seam sealed                                                                                                                                                                                                                                                  |                       |
| Assembly screening (100%)                    | Stabilization bake - 24h @ 150°C / MIL-STD-750 method 1032<br>Temp. Cycling - 20 cycles -55/+150°C / MIL-STD-750 method 1051<br>PIND test - MIL-STD-750 method 2052<br>X-ray - Wire bonding inspection / MIL-STD-883 2009.12<br>Leak test - MIL-STD-883-1014.15 Cond A2 |                       |
| <b>ELECTRICAL SCREENING FLOW</b>             |                                                                                                                                                                                                                                                                         |                       |
| Manufacturer                                 | UMS – Villebon – France                                                                                                                                                                                                                                                 |                       |
| Burn In 1<br>Electrical Stabilization (100%) | 10hrs - Tamb=+125°C; VD=+50V; VG=-7V<br>10hrs - Tj=200°C; VD=+50V; VG=cst (RIC1:ID=256mA / RIC3:ID=1.25A)                                                                                                                                                               |                       |
| Burn-In 2 (100%)                             | 240hrs - Tj=200°C; VD=+50V; VG=cst (RIC1:ID=256mA / RIC3:ID=1.25A)<br>RF characterization / Acceptance criteria based on specification and on parameter drift (before/after burn-in) / Pulsed or CW                                                                     |                       |

| FINAL PRODUCT                                             |                                                                                                                                                                                                          |
|-----------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Product overview                                          |                                                                                                                        |
| Typical RF-power<br>@1.3GHz<br>(Vds=50V,<br>Idq=25mA/mm ) | <div style="display: flex; justify-content: space-around;"> <div style="text-align: center;"> <p>&gt;15W</p> </div> <div style="text-align: center;"> <p>&gt;100W</p> </div> </div>                      |
| Product reference                                         | <div style="display: flex; justify-content: space-around;"> <div style="text-align: center;"> <p><b>CHK8101-SYC</b></p> </div> <div style="text-align: center;"> <p><b>CHKA012bSYA</b></p> </div> </div> |

Radiation characteristics

Displacement Damage (DD) tests show no influence of proton irradiation on the electrical performance up to a tested fluence of  $10^{12}$  p/cm<sup>2</sup>.

Total Ionizing Dose (TID) tests show no influence of Co60 irradiation up to a final tested dose of 274 krad (in GaN).

Single Event Burn-out (SEB) levels for gate and drain RF voltage excursions for transistors operating in class AB, such as described in next table.

| Characteristics                   | Symbols        | Safe Operating Area | Units | Remarks                                                                      |
|-----------------------------------|----------------|---------------------|-------|------------------------------------------------------------------------------|
| Drain-Source Voltage RF-Excursion | $V_{DS\_peak}$ | $\leq 125$          | V     | Xe ions (LET-Si = 62.5keV/ $\mu$ m)<br>Fluence = 1.E+07 ions/cm <sup>2</sup> |
| Gate-Source Voltage RF-Excursion  | $V_{GS\_peak}$ | $\geq -6$           | V     |                                                                              |
| Drain-Source Voltage RF-Excursion | $V_{DS\_peak}$ | $\leq 125$          | V     | Rh ions (LET-Si = 46.1keV/ $\mu$ m)<br>Fluence = 1.E+07 ions/cm <sup>2</sup> |
| Gate-Source Voltage RF-Excursion  | $V_{GS\_peak}$ | $\geq -9$           | V     |                                                                              |

In vacuum environment characteristics

Corona/Multipaction free operation test campaign was performed. No failure occurred up-to the maximum levels that it was possible to evaluate and which are provided in table here after.

| Characteristics                   | Symbols        | Safe Operating Area | Units | Remarks                              |
|-----------------------------------|----------------|---------------------|-------|--------------------------------------|
| Drain-Source Voltage RF-Excursion | $V_{DS\_peak}$ | $\leq 133$          | V     | Multipaction Phenomena<br>See Note 1 |
| Drain-Source Voltage RF-Excursion | $V_{DS\_peak}$ | $\leq 138$          | V     | Corona Phenomena<br>See Note 2       |

**NOTES:**

- 1) Multipaction test conditions: Pressure  $< 1.5 \cdot 10^{-5}$  mbar, frequency = 1.25GHz, Test board temperature from -30°C to 70°C
- 2) Corona test conditions: Pressure from 900mbar to  $< 1.5 \cdot 10^{-5}$  mbar, frequency = 1.25GHz, Test board temperature from -30°C to 70°C

**5.2.4.5 Manufacturing sites****WAFER FABRICATION:**

UMS Gmbh Wilhelm Runge Strasse 11 D-089081 Ulm Germany

**DIE ASSEMBLY:**

RHe Microsystems Gmbh Heidestrass 70 01454 Radeberg Germany

**CONTROL AND TEST:**

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