

		APPLICATION FOR ESCC QUALIFICATION APPROVAL			Page 1	
Component Title: Radiation Tolerant Single Port Gigabit Ethernet Copper PHY (VSC8541RT)		Executive Member: CNES			Date: 12/05/2025	
Appl. No. 392						
Components (including series and families) submitted for Qualification Approval						
1						
Components (including series and families) submitted for Qualification Approval						
1						
ESCC COMPONENT. NO. VARIANTS RANGE OF COMPONENTS BASED ON TEST VEHICLE / S COMPONENT SIMILAR						
9405/020 01 Radiation Tolerant Single Port Gigabit Ethernet Copper PHY VSC8541RT – Manufactured on a 65nm, eight-metal-layers CMOS technology in TSMC, Taiwan, Packaged in a hermetic CQFP68 and assembled in Microchip Thailand. VSC8541RT CQFP68 NA						
Component Manufacturer 2 Location of Manufacturing Plant 3 ESCC Specification used for Qualification 4						
MICROCHIP TECHNOLOGY NANTES		LA CHANTRERIE – ROUTE DE GACHET BP70602 44306 NANTES CEDEX		Generic: ESCC9000 Issue 11 Detail/s: 9405/020 Issue 2		
Qualification Report Reference and date: 5 PID used for manufacturing Qualification Lot 6						
QP_VSC8541RT_revD		Ref No: PID0043 Issue: 0 Date: 21/03/2025				
Date: 01/09/2024						
PID changes since start of qualification 7 Current PID Verified by CNES 8						
None <input checked="" type="checkbox"/> Minor* <input type="checkbox"/> Major* <input type="checkbox"/> (* Details not published, provided in confidential annex 2.)		Name of Executive Representative VSC8541RT PID 0043 Ref No: VSC8541RT PID 0043 Issue 0 Date 21/03/2025				
Current Manufacturing facilities surveyed by: 9						
CNES (D. Dangla) ESA (S. Hernandez)		16/10/2024				
(Name of Executive Responsible)		(Date)				
Report Reference DTN QE EC-2024.0014739 - CR-ESCC QML survey MCHP-16102024.pdf						
Satisfactory: Yes <input checked="" type="checkbox"/> No <input type="checkbox"/> Explain						
Quality and Reliability Data 10						
Evaluation testing performed Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>		Failure analysis, DPA, NCCS available Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>				
Report Ref. No.:		(supply data)				
Date:						
Equivalent Data: Single Phase Qualification applies						
Certification:		Ref Nos. and purpose: Construction analysis reports done by MCHP/SERMA: 20-2502-100_approved 2020-08-09.pdf (August 9th, 2020)				

	APPLICATION FOR ESCC QUALIFICATION APPROVAL Component Title: Radiation Tolerant Single Port Gigabit Ethernet Copper PHY (VSC8541RT) Executive Member: CNES Date: 12/05/2025	Page 2 Appl. No. 392
<p>The undersigned hereby certifies on behalf of the ESCC Executive, that the above information is correct; that the appropriate documentation has been evaluated; that full compliance to all ESCC requirements is evidence except as stated in box 13; that the reports and data are available at the ESCC Executive and therefore applies for ESCC qualification status to be given to the component(s) listed herein.</p> <p>Date: 12/05/2025</p> <div>Fontaine Lya Signature numérique de Fontaine Lya Date : 2025.05.12 15:07:07 +02'00' L. FONTAINE (Signature of the Executive Coordinator)</div>		11
Continuation of Boxes above: (Only non-confidential comments)		12
<p>Qualification Package VSC8541RT - QP_VSC8541RT_revD.pdf</p> <p>VSC8541RT ESCC QPL - submission 2025-02 rev1.pptx RAD-VSC8541RT_rev4.pdf</p> <p>Construction analysis reports done by MCHP/SERMA: 20-2502-100_approved 2020-08-09.pdf (August 9th, 2020)</p>		

	APPLICATION FOR ESCC QUALIFICATION APPROVAL		Page 3
Component Title: Radiation Tolerant Single Port Gigabit Ethernet Copper PHY (VSC8541RT)		Appl. No.	
Executive Member:		Date:	392
Non compliance to ESCC requirements:			13
No.:	Specification	Paragraph	Non compliance
Additional tasks required to achieve full compliance for ESCC qualification or rationale for acceptability of noncompliance:			14
Executive Manager Disposition			15
Application Approval: Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>			
Action / Remarks:			
Date:			
<div>Ali Zadeh</div> <div>Digitally signed by Ali Zadeh Date: 2025.06.02 10:35:09 +02'00'</div> <div>A.Zadeh: Head of the Avionics and EEE Division</div>			

		APPLICATION FOR ESCC QUALIFICATION APPROVAL		Page 4
Component Title: Radiation Tolerant Single Port Gigabit Ethernet Copper PHY (VSC8541RT)		Executive Member:		Appl. No. 392
ANNEX 1: LIST OF TESTS DONE TO SUPPORT QUALIFICATION		Date:		16
Tests conducted in compliance with: <ul style="list-style-type: none"> – ESCC 9000 generic specification; Chart F4 (for ESCC/QPL parts); – Or PID-TFD [REDACTED] (for ESCC/QML parts) 				
Tests vehicle identification/description:				
VSC8541RT CQFP-68		<p>The VSC8541RT device is a radiation tolerant single port Gigabit Ethernet copper PHY targeting space-constrained 10/100/1000BASE-T applications. It withstands the harsh aerospace environment with enhanced radiation performances, extreme temperatures, vacuum and high reliability. It features integrated, line-side termination to conserve board space, lower EMI, and improve system performance. Additionally, integrated RGMII timing compensation eliminates the need for on-board delay lines. The device supports the industry's widest range of LVCMOS levels for a parallel MAC interface including: 1.5 V, 1.8 V, 2.5 V, and 3.3 V, as well as 1.2 V, 1.5 V, 1.8 V, 2.5 V, and 3.3 V support on the MDIO/MDC interface.</p> <p>The VSC8541RT die, in its hermetic CQFP68 version, is bonded with Au wire of 25.4µm diameter, with pad metallization in Al.</p> <p>The minimum separation between 2 Au wires is one wire diameter (25.4µm) instead of 2 diameters (TM2010A).</p> <p>Static Burn-in does not apply.</p> <p>No seal test has been performed after Life-Test.</p>		
Detail Specification reference: 9405/020 issue 2				



APPLICATION FOR ESCC QUALIFICATION APPROVAL

Component Title: **Radiation Tolerant Single Port Gigabit Ethernet Copper PHY (VSC8541RT)**

Executive Member:

Date:

Page 5

Appl. No.

392

Endurance subgroup

Note:

Life test is done on each wafer lot. One wafer lot has been qualified.

The lot#1 has been submitted to extended life test, up to 4000 hours/125°C/Vccmax.

No seal test has been performed after life test.

Chart F4	Test	Tick when done	Conditions	Diffusion Lot Assembly lot Date code	Tested Qty per lot	No. of Rejects	Comments if not performed. Comments on Rejection
Endurance Subgroup	Operating Life-Test	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005	PPMG74.00 19Q3	45	0	4000 hrs/Vccmax/125°C
	Intermediate and End-Point Electrical Measurements	<input checked="" type="checkbox"/>	Intermediate and End-Point Electrical Measurements in the Detail Specification		(45)	0	

Assembly/Packaging capability is verified on each assembly lot.

- Solderability test, required by ESCC, is done on each assembly lot

- Terminal strength test, required by ESCC every 2-years, is done every 52-weeks per package family (*)

Chart F4	Test	Tick when done	Conditions	Date Code Diffusion Lot	Tested Qty per lot	No. of Rejects	Comments if not performed. Comments on Rejection
Assembly Capability Subgroup	Marking Permanency	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2015	PPMG7A2AJK DC2307	3#	0	
	Bond Pull / Ball Shear tests	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2011 JESD22-B116		4#	0	100% / 50% wires
	Bond Pull / Ball Shear tests After Bake 300°C / 1h	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2011 JESD22-B116	PPMG7A2AKA DC2314	4#	0	100% / 50% wires Specific for Gold wire bonding as preconized by MIL-PRF-38534 for hybrids
	Substrate attach strength	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2027	PPMG7A2CRC DC2320	3#	0	
	Internal Visual Inspection	<input checked="" type="checkbox"/>	MIL-STD-883 Test Method 2010A	PPMG7A2637 DC2342	2#	0	
	Solderability test	<input checked="" type="checkbox"/>	MIL-STD-883 Test Method 2003		3#	0	22 leads
	Terminal strength test	<input checked="" type="checkbox"/>	MIL-STD-883 Test Method 2004 B2	PPMG7A28BQ DC 2424 PCC06A28FD DC 2441	3#		45 leads

Chart F4	Test	Tick when done	Conditions	Date Code Diffusion Lot	Tested Qty	No. of Rejects	Comments if not performed. Comments on Rejection
Additional Tests	HBM ESD	<input checked="" type="checkbox"/>	ANSI/ESDA/JEDEC JS-001	1606AAZPA	3#	0	Ok up to 2000V Class 2
	CDM ESD	<input checked="" type="checkbox"/>	ANSI/ESDA/JEDEC JS-002	PPMG74.4	3#	0	Ok up to 500V Class C2a
	Electrical latch-up	<input checked="" type="checkbox"/>	JESD78	1606AAZPA	6#	0	Current injection 100mA, Overvoltage 1.5*Vccmax Class II : 125°C
	Pre seal Bond Pull / Ball Shear tests	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2011 JESD22-B116	PPMG7A2AKA PPMG7A2CRC PPMG7A2637 PPMG7A265J PPMG7A266S PPMG7A28BG	2# 2# 2# 2# 2# 2#	0 0 0 0 0 0	80% / 80% wires Specific for Gold wire bonding: Specificities of Assembly flow: Bonding controls before encapsulation
	Pre seal Bond Pull / Ball Shear tests After Bake 300°C / 1h	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2011 JESD22-B116	PPMG7A2AKA PPMG7A2CRC PPMG7A2637 PPMG7A265J PPMG7A266S PPMG7A28BG	2# 2# 2# 2# 2# 2#	0 0 0 0 0 0	Pre-bake before lid sealing limited to 150°C/4 hrs No stabilization bake (150°C/24 hrs)
	Operating Life-Test 1000hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005	PPMG7A282R DC2025	24#	0	Specific for Gold wire bonding: For Bonding evaluation after Life test
	Operating Life-Test 2000hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005		18#	0	
	Intermediate and End-Point Electrical Measurements	<input checked="" type="checkbox"/>	Intermediate and End-Point Electrical Measurements in the Detail Specification		24# 18#	0 0	
	Operating Life-Test 1500hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005	PPMG7A284M DC2034	24#	0	
	Operating Life-Test 2000hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005		24#	0	
	Intermediate and End-Point Electrical Measurements	<input checked="" type="checkbox"/>	Intermediate and End-Point Electrical Measurements in the Detail Specification		24# 24#	0 0	
	Operating Life-Test 1000hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005	PPMG7FBTAA DC2035	24#	0	
	Operating Life-Test 2000hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 1005		18#	0	
	Intermediate and End-Point Electrical Measurements	<input checked="" type="checkbox"/>	Intermediate and End-Point Electrical Measurements in the Detail Specification		24# 18#	0 0	
	Bond Pull / Ball Shear tests After Life-Test 1000hrs/125°C* And 2000hrs/125°C	<input checked="" type="checkbox"/>	MIL-STD-883, Test Method 2011 JESD22-B116	PPMG7A282R DC2025 PPMG7A284M DC2034 PPMG7FBTAA DC2035	5# 5# 5#	0 0 0	Specific for Gold wire bonding: 100% / 50% wires (*): after 1500 hours for the lot PPMG7A284M DC2034
	TID	<input checked="" type="checkbox"/>	ESCC22900	PPMG74.00	27#	0	Ok 100 krad(Si) (22 ON and 5 OFF parts)

	SEL	<input checked="" type="checkbox"/>	JESD57, ESCC25100	PPMG74.00	3# 3#	0 0	SEL LET threshold > 78 MeV.cm²/mg. Note: For SEU/SEFI, refer to "RAD-VSC8541RT_rev4.pdf" rad test report
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