

RF/MIXED SIGNAL ASIC Based on Rad-Hard XH018 IP Library				387
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Basic ESCC 2439000 Generic ESCC 9000 Detail ESCC 9202/084	IMST, Kamp-Lintfort, Germany	Qualification	DLR	February 2024
ESCC Capability approval qualification.				
Capability Domain Abstract: 7000/SPAC/IMST_RD_020, version 1.5				
Qualified variants:				
Detail spec	Variant Number	Case	Terminal Material and Finish	Weight max g
9202/084	01	CQFN-256	D2	11
9202/084	02	CQFN-132_A	D2	5.5
Operating Temperature Range (°C): -40 to +125				

6.8.2 PLL/ SYNTHESIZER

INTEGRATED CIRCUITS, MONOLITHIC, CMOS AND HBT ON SiGe:C, 1.6 TO 12 GHz FREQUENCY SYNTHESIZER WITH INTEGRATED SIGMA DELTA MODULATOR FRACTIONAL-N PLL, BASED ON TYPE NOV1G14				377
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic ESCC 9000 Detail ESCC 9202/085	IMST, Kamp-Lintfort, Germany	Qualification	DLR	September 2025
		Remarks: The component is sensitive to assembly process. It is recommended to perform a SMT verification according to ECSS-Q-ST-70-38 Rev1 with adapted parameter to the mission needs. Information is available in the appendix A of ESCC 9202/085		
Qualified variants:				
Detail spec	Variant Number	Case	Terminal Material and Finish*	Weight max g
9202/085	01	CQFN-32	14	0.2
Operating Temperature Range (°C): -55 to +125				