

INTEGRATED CIRCUITS, SILICON, MONOLITHIC, 35KLUT RADIATION-HARDENED FPGA (NG-MEDIUM)				382A
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic ESCC 9000 Detail ESCC 9304/010 9202/086	NanoXplore, Sèvres, France	Qualification	CNES	Aug 2022
Remarks				

Qualified range:

Detail spec	Variant Number	Based on Type	Case	Terminal Material and Finish	Weight max g
9304/010	01	NX1H35AS	CQFP-352	D2	28.6

Based on STM C65SPACE ASIC platform technology. Qualified domain is listed in QML Technology Description in REP006.

Operating Temperature Range (°C): -55 to +125

INTEGRATED CIRCUITS, SILICON, MONOLITHIC, 536KLUT RADIATION-HARDENED SoC FPGA (NG-ULTRA)				395
Procurement Specifications	Manufacturer	Nature of Approval	Supervising Authority	Initial Qualification Date
Generic ESCC 9000 Detail ESCC 9304/012	NanoXplore, Sèvres, France	Qualification	CNES	Nov 2025
		Remarks		
Qualified range:				
Detail spec	Variant Number	Based on Type	Case	Weight max g
9304/012	01	NX2H540ATSC-FF1760 (NG-ULTRA)	Fine pitch Flip-Chip Organic Ball Grid Array with 1760 balls (FF1760)	42
Based on STM ASIC 28nm FDSOI platform technology. Qualified domain is listed in QML Technology Description in REP006.				
Operating Temperature Range (°C): -40 to +125				